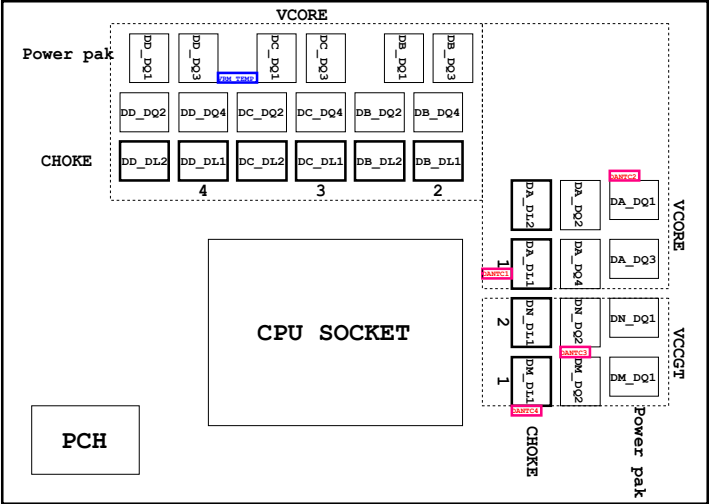



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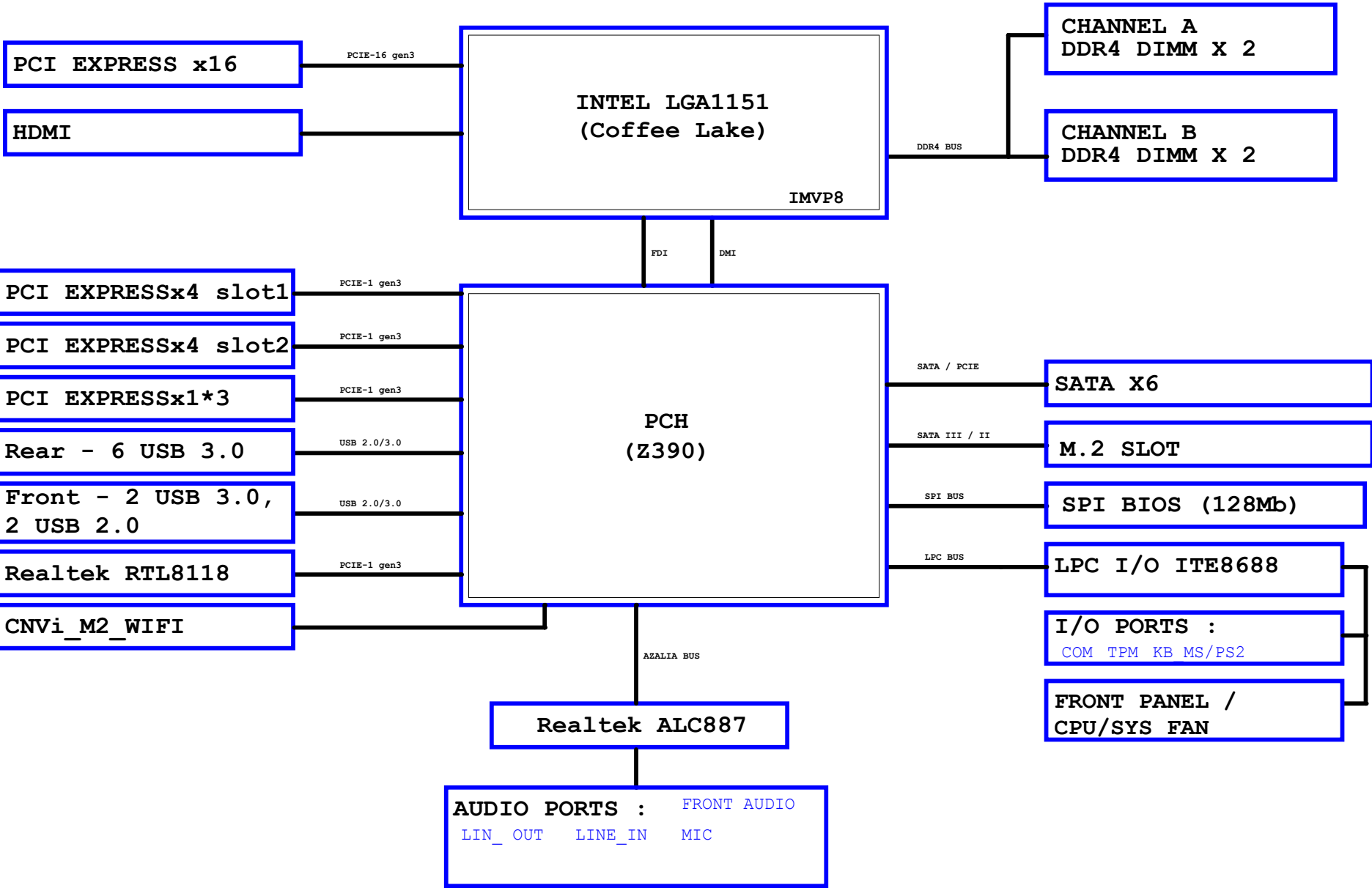
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01	COVER SHEET	
02	BOM & PCB MODIFY HISTORY	
03	BLOCK DIAGRAM	
04	CPU LGA1151-A	
05	CPU LGA1151-B-DDR4	
06	CPU LGA1151-C	
07	CPU LGA1151-D	
08	DDR 4 CHANNEL A	
09	DDR 4 CHANNEL B	
10	PCH CLOCK/DDC/BIOS	
11	PCH DMI,USB,PCIE	
12	PCH MISC	
13	PCH SATA,PCIE,CNVI	
14	PCH PWR	
15	PCH GND,Heatsink	
16	ITE 8688 LPC IO RGB LED	
17	HWM	
18	FAN CTRL-CFL-SIO	
19	Dual BIOS for CS mode	
20	CEC Logic	
21	PCIEX16 SLOT	
22	PCIEX4 SLOT1	
23	PCIEX4 SLOT2	
24	PCIEX1*3 SLOT	
25	SATA	
26	M.2 x4 (A)	
27	COM, TPM, THB	
28	VCORE ISL69138 (PWM)	
29	VCORE ISL69138 (Vcore PPAK-1)	
30	VCORE ISL69138 (Vcore PPAK-2)	
31	VCORE ISL69138 (Vcore PPAK-3)	
32	VCORE ISL69138 (VccGT PPAK)	
33	VCCSA VCCIO VCCPLL	
34	RT8120 DDR CHOKE-IRON-2L	
35	RT8120 VPP CHOKE-IRON-合金	
36	RT8120 PCH-CHOKE-IRON	
37	DISCRETE POWER	

SHEET	TITLE
38	ATX POWER , -PROCHOT
39	NCP3933 OVER VOLTAGE
40	HDMI NO LS/R USB30
41	R USB30 ,KB MS
42	CNVi M2 WIFI
43	REALTEK RTL8118
44	USB30 LAN CONNECTOR-8118
45	Realtek ALC887
46	REAR AUDIO JACK
47	F USB30
48	F USB
49	F PANEL ,OC PIN
50	Audio / DEBUG / XMP LED
51	EMI-ESD
52	POWER MAP
53	NTC MAP

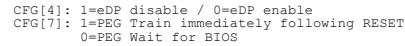
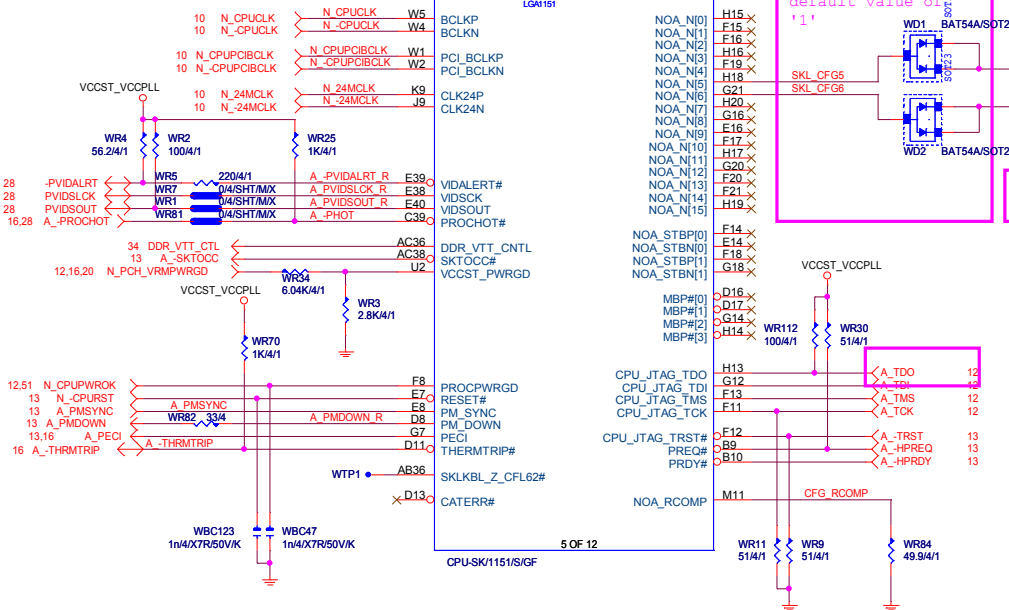


			
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BOM & PCB MODIFY HISTORY			
Size	Document Number	Rev	
Custom	Z390 UD	1.15	
Date:	Monday, March 30, 2020	Sheet	2 of 53

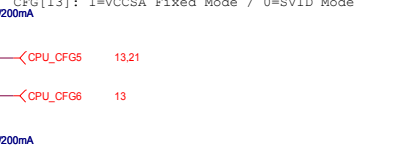
BLOCK DIAGRAM



CFL R0.8

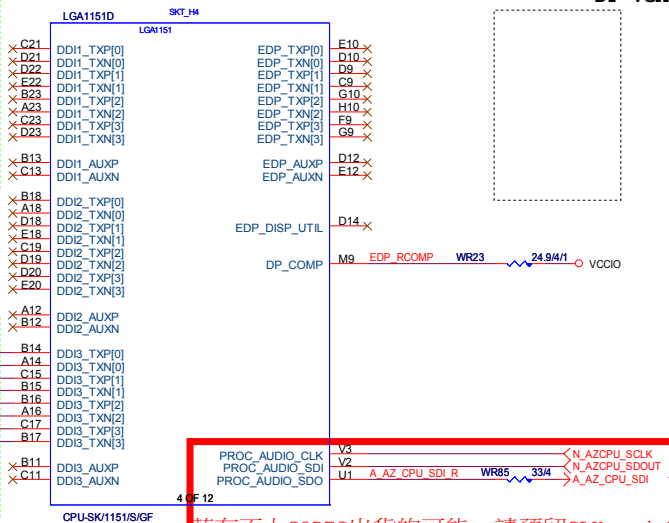
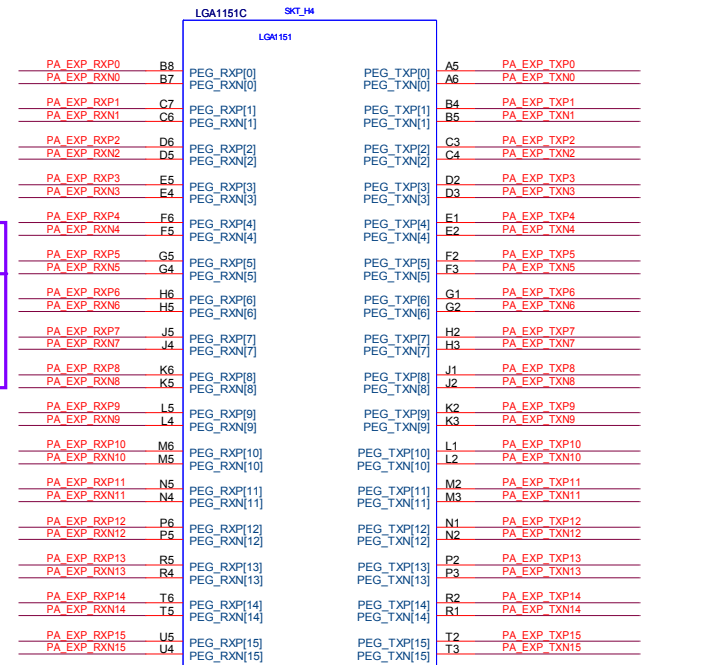


CFG[13]: 1=VCCSA Fixed Mode / 0=SVID Mode



非x8/x4/x4設計的可移除

Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0



若有不上CODEC出貨的可能，請預留CLK and SDO對地電阻。

```
G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R
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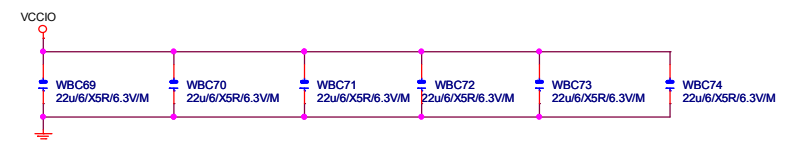
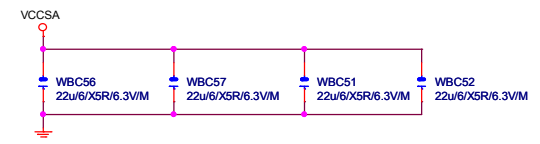
Gigabyte Technology

CPU LGA1151-A

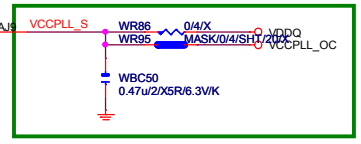
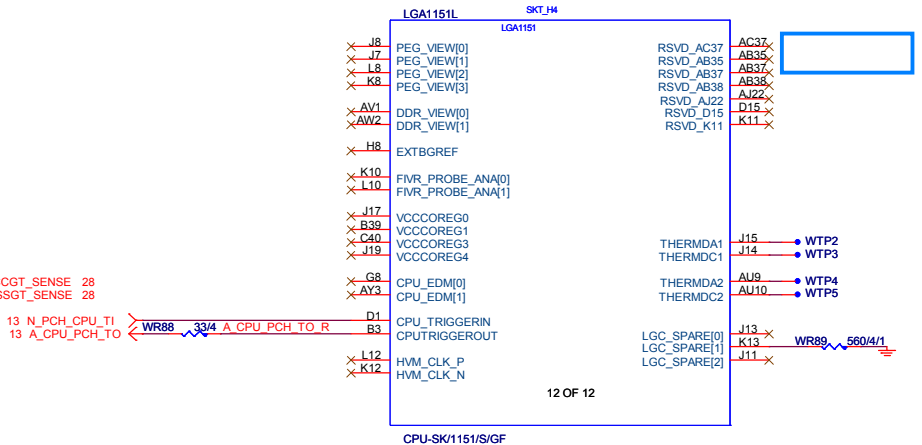
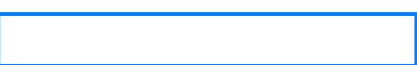
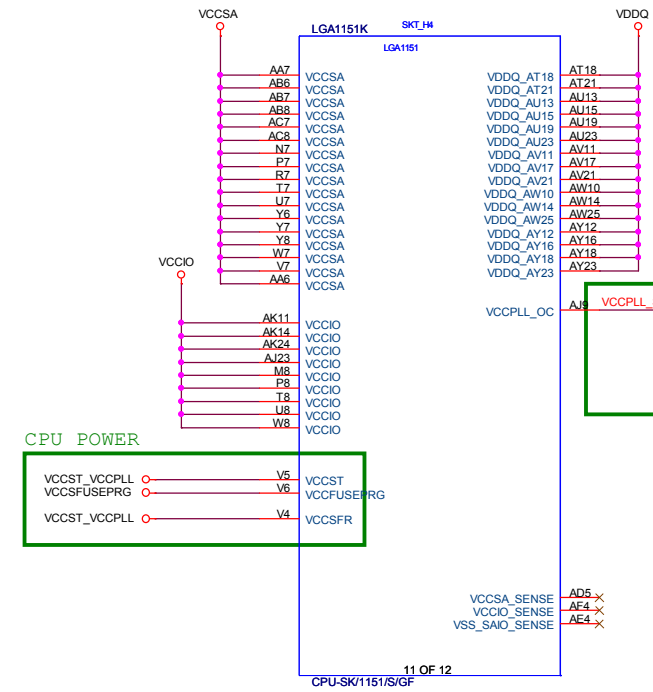
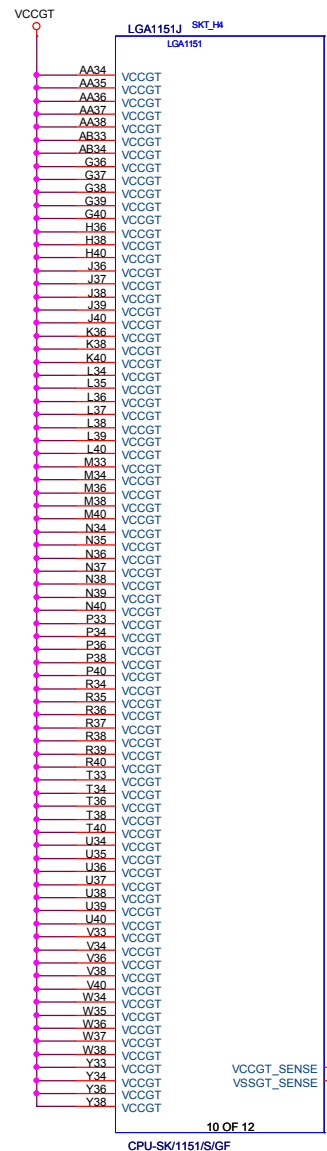
Size Custom	Document Number Z390 UD
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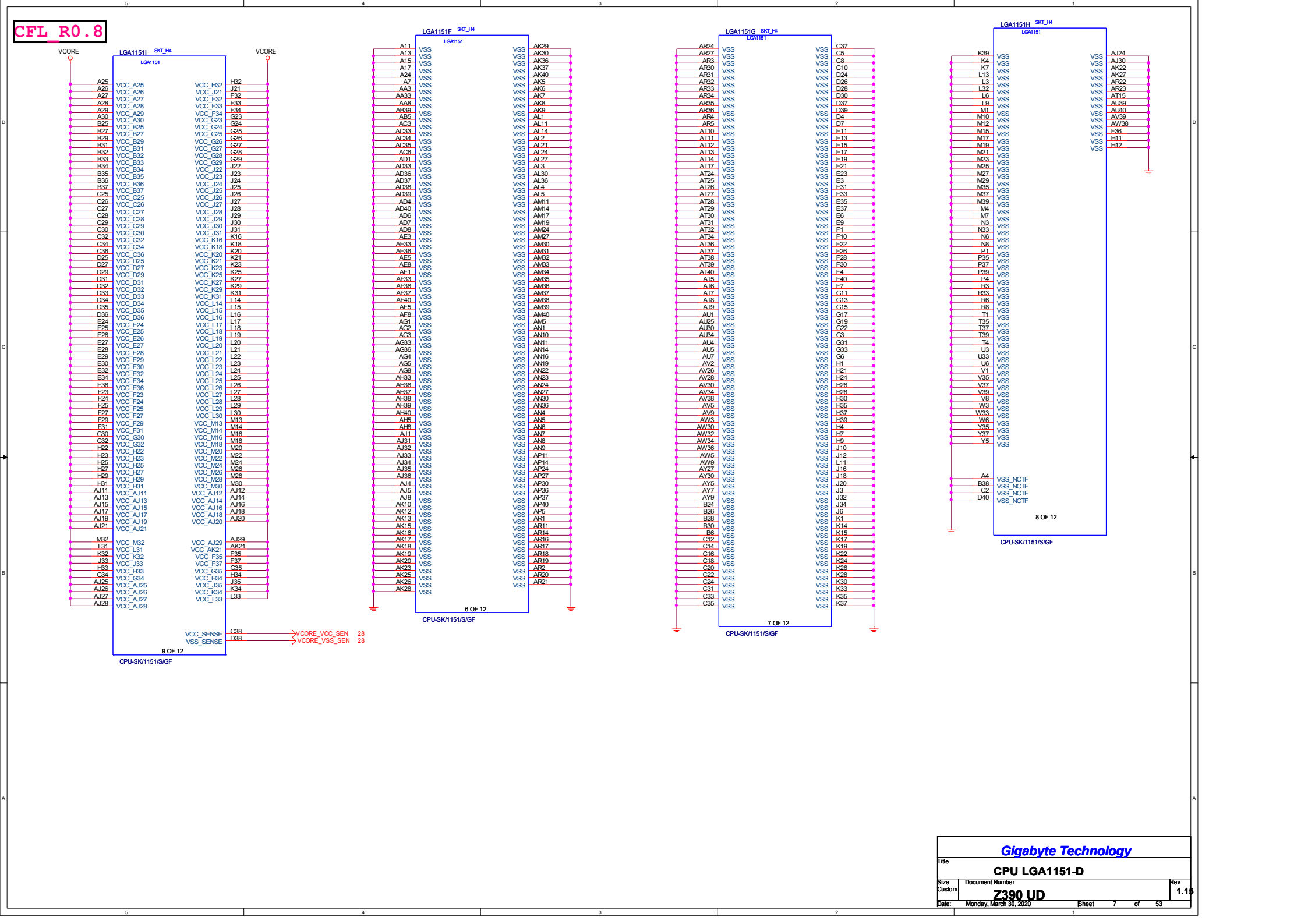
Date: Monday, March 30, 2020 Sheet 4 of 53

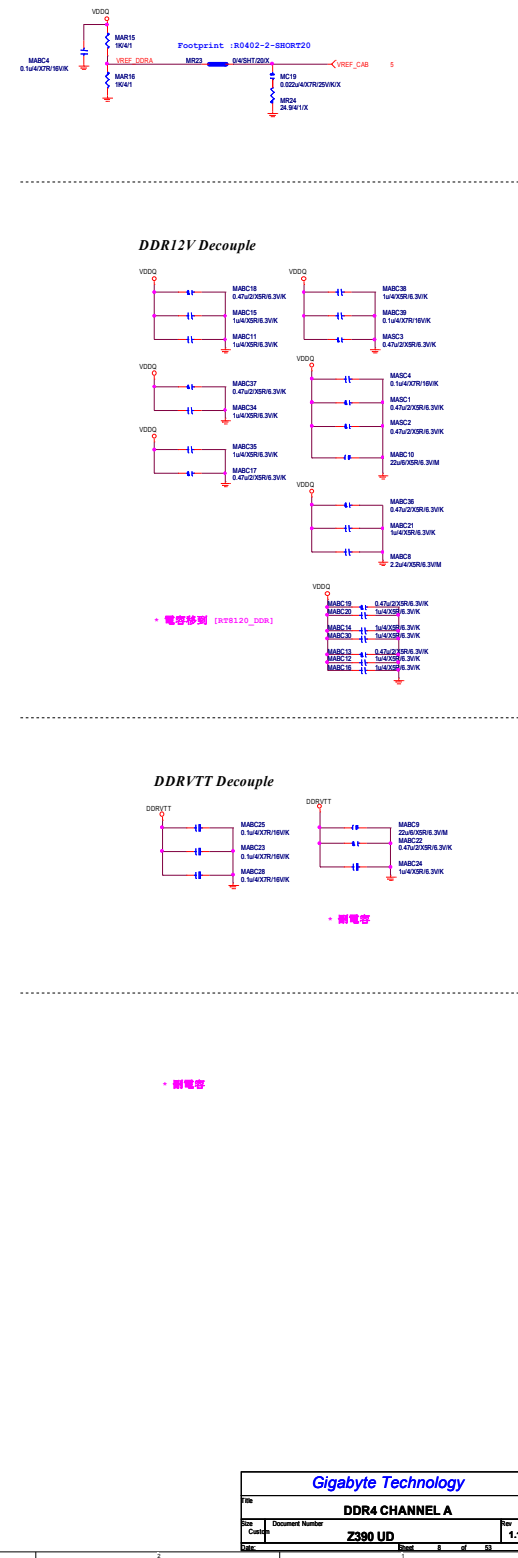
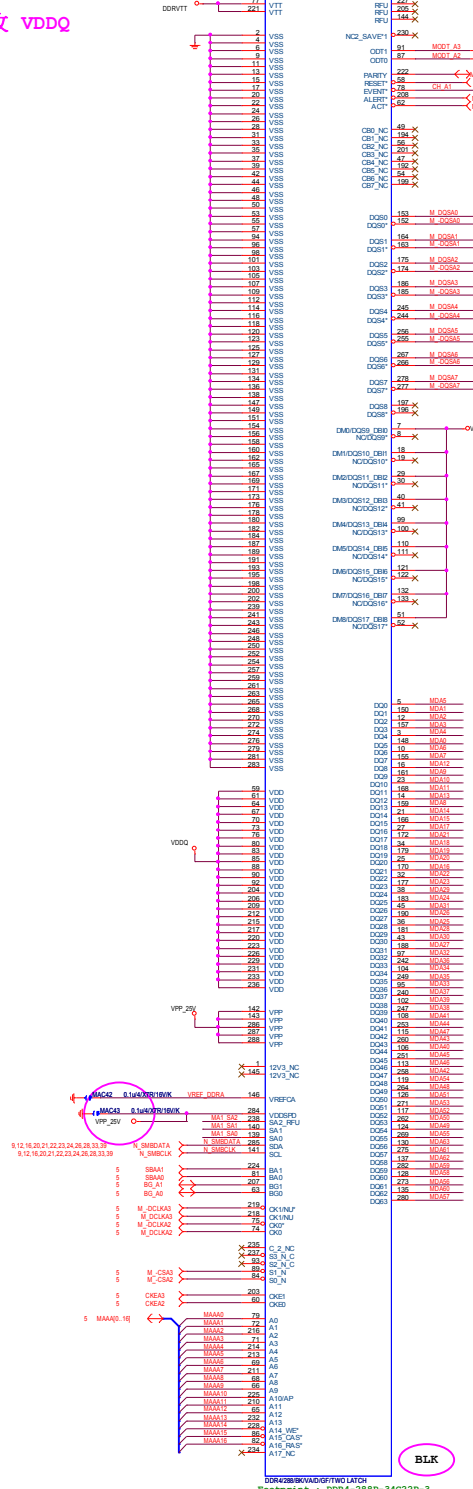
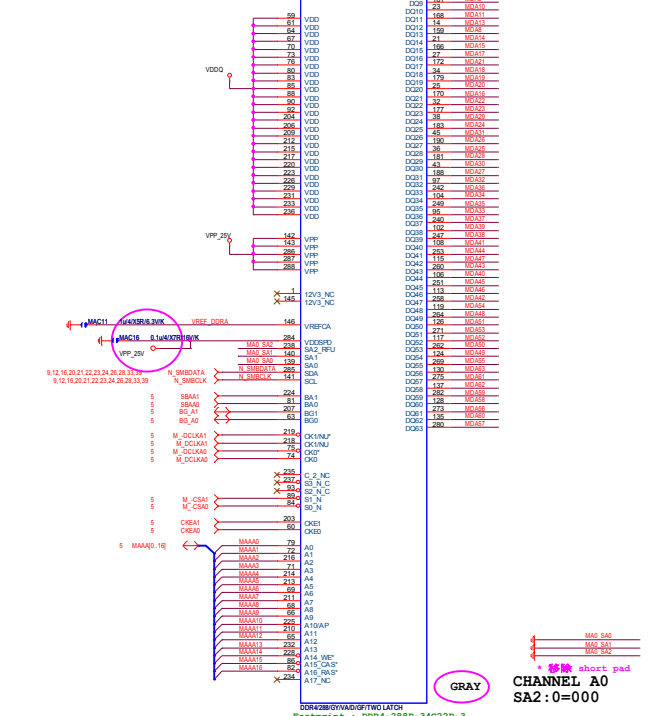
CFL_R0.8 check 模組

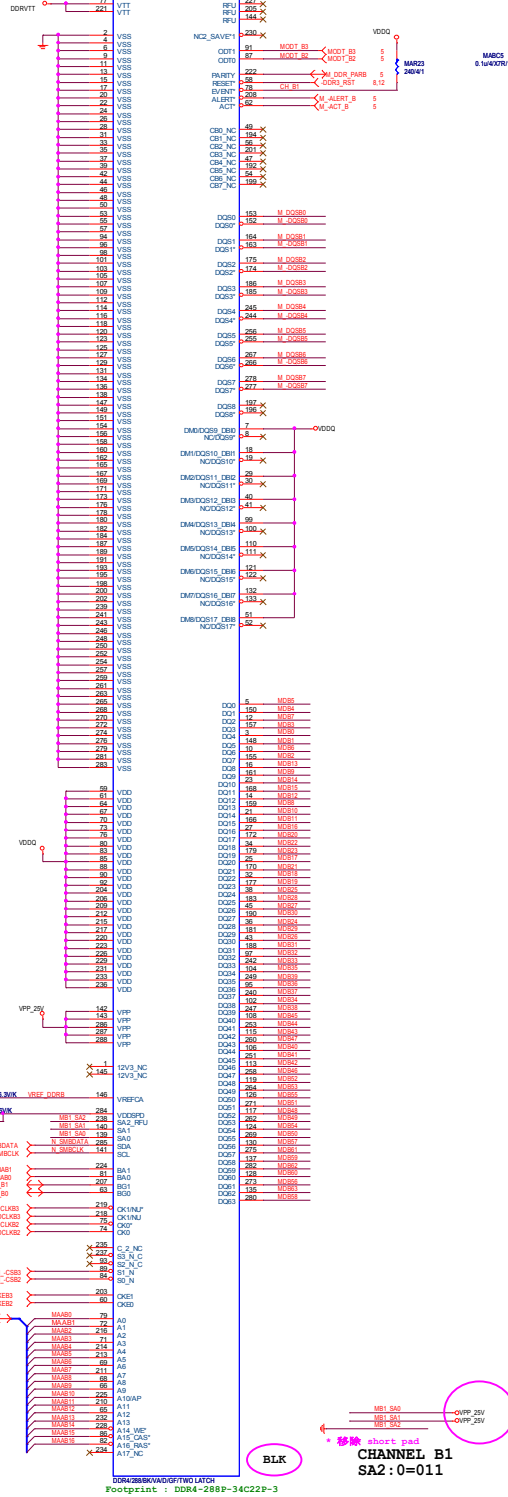
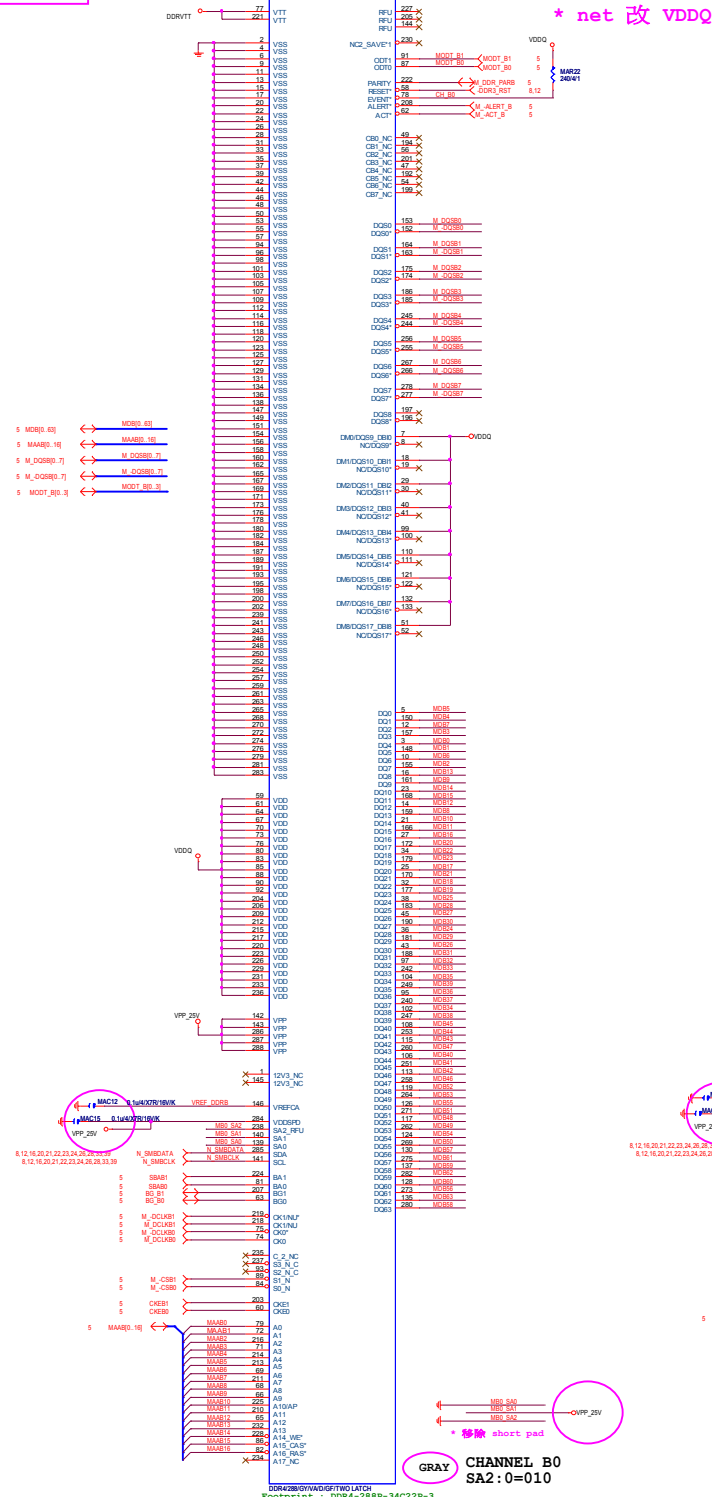


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注意左側Table及下方訊號名稱改USB31

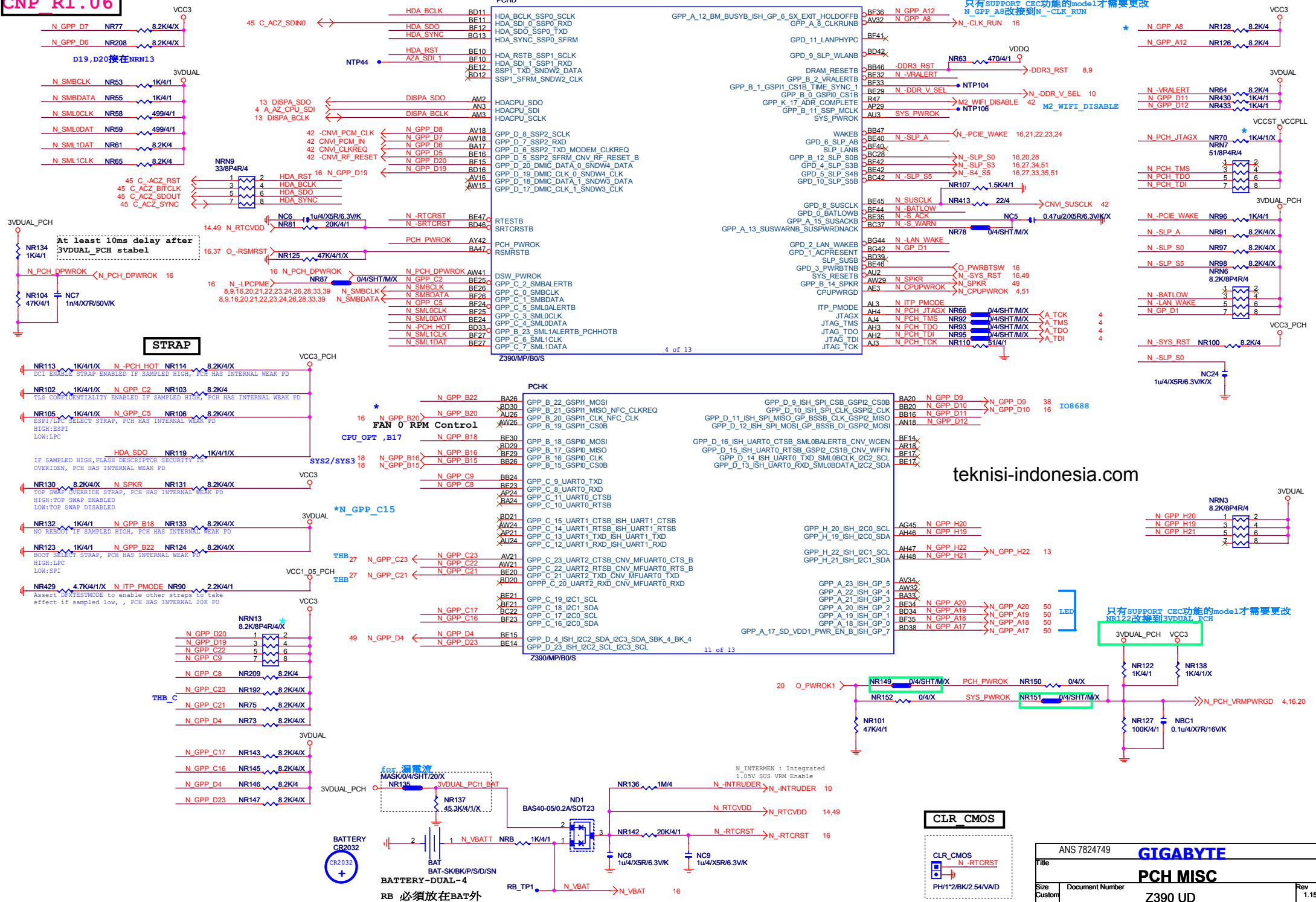
GEN2

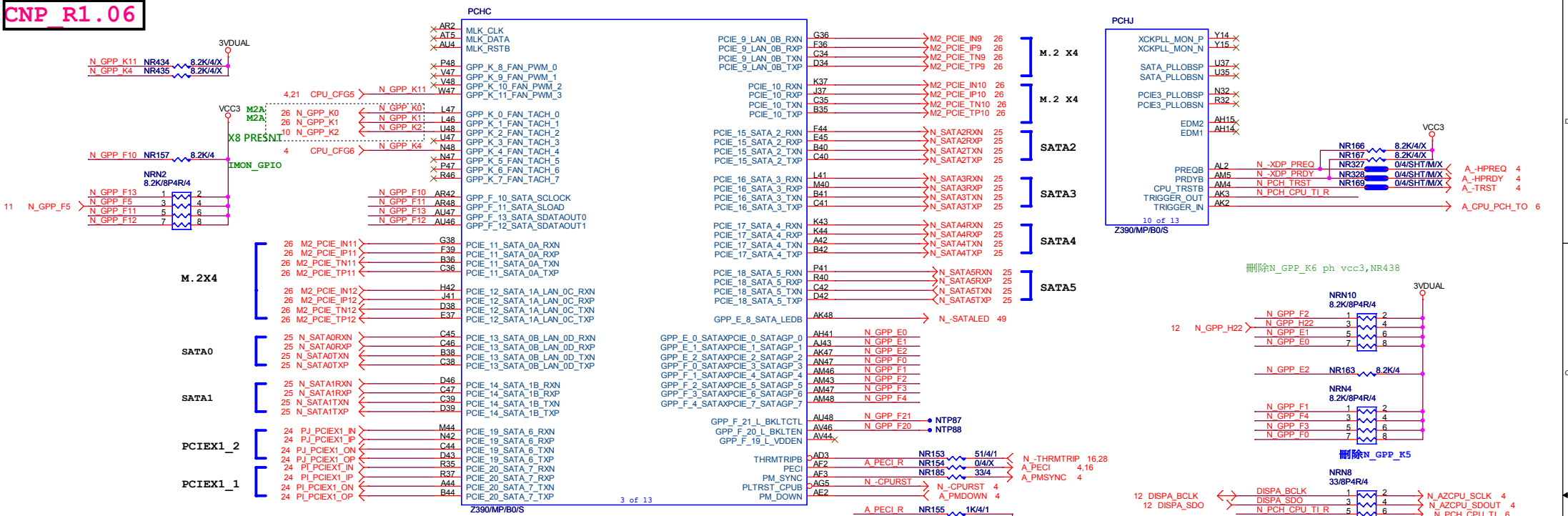


H370 PCIE
only

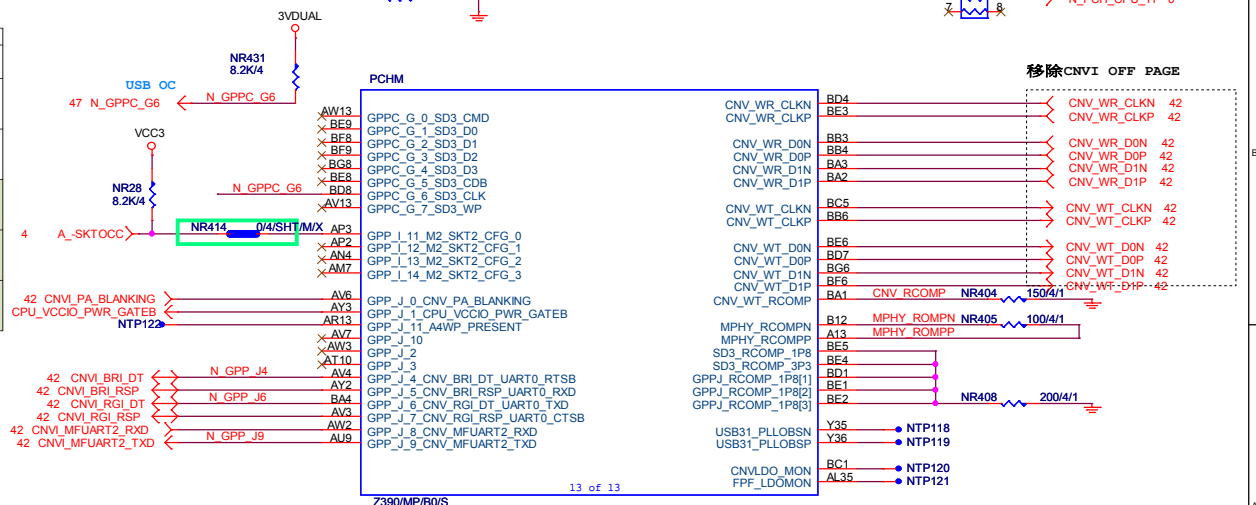
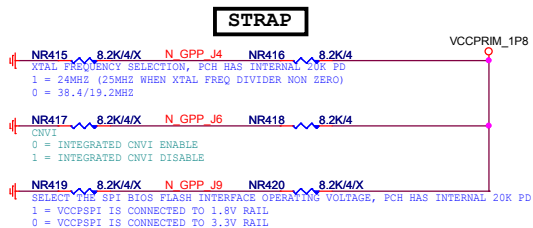


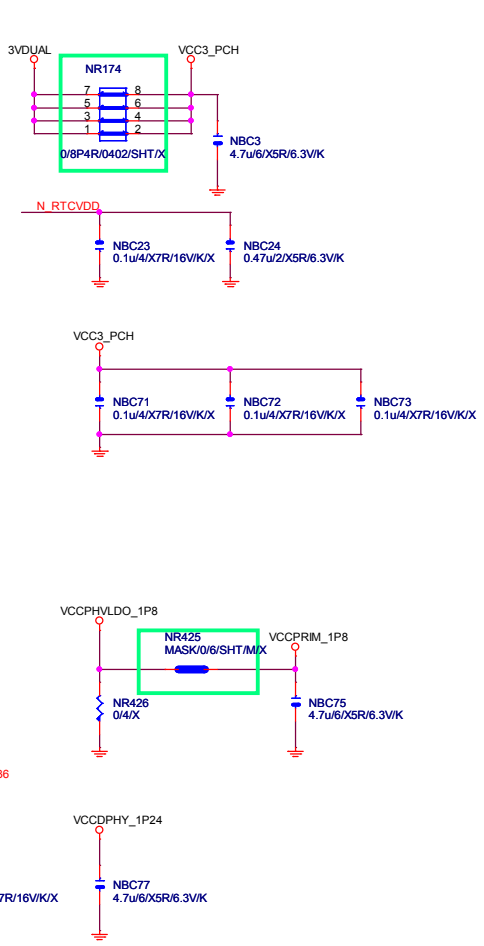
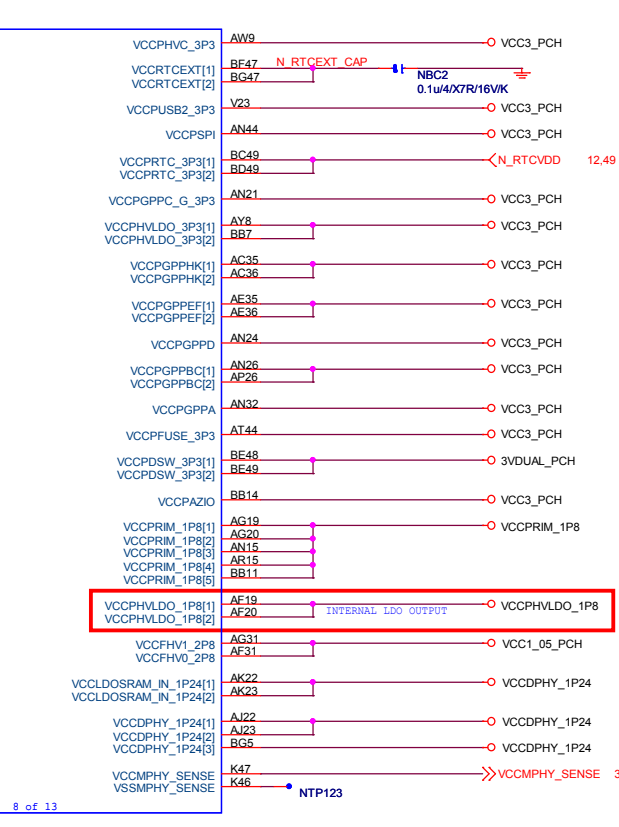
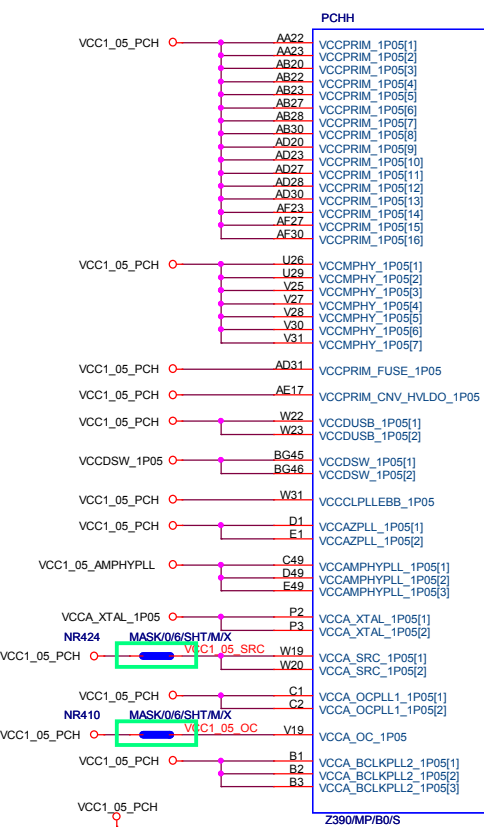
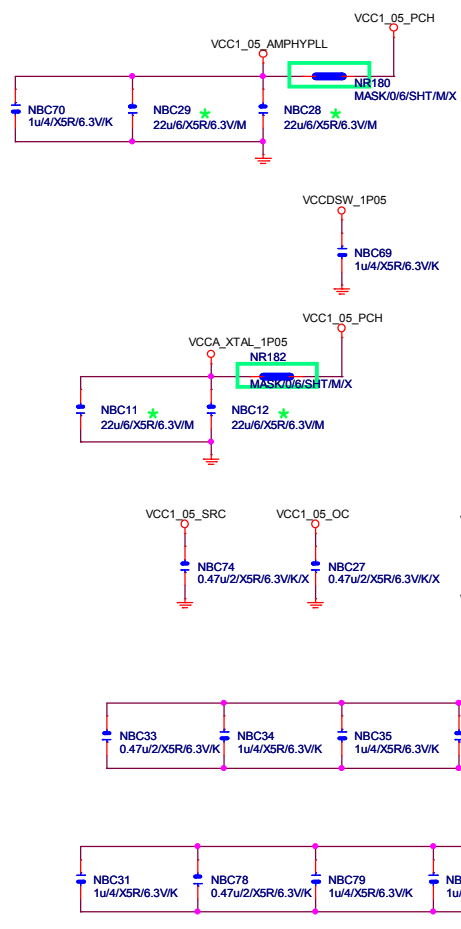
CNP R1.06





ITEM	PCIE P9	PCIE P10	PCIE P11	PCIE P12	PCIE P13	PCIE P14	PCIE P15	PCIE P16	PCIE P17	PCIE P18	PCIE P19	PCIE P20
H310	GbE	N/A	PCIE	PCIE GbE	GbE SATA 0	SATA 1	SATA2	SATA 3	N/A	N/A	N/A	N/A
B360	PCIE GbE	PCIE	PCIE SATA 0A	PCIE GbE SATA 1A	GbE SATA 0B	SATA 1B	SATA2	SATA 3	SATA4	SATA5	N/A	N/A
Q360	PCIE GbE	PCIE	PCIE SATA 0A	PCIE GbE SATA 1A	PCIE GbE SATA 0B	PCIE SATA 1B	SATA2	SATA 3	SATA4	SATA5	N/A	N/A
H370	PCIE GbE	PCIE	PCIE SATA 0A	PCIE GbE SATA 1A	PCIE GbE SATA 0B	PCIE SATA 1B	PCIE SATA 2	PCIE SATA 3	SATA4	SATA5	PCIE	PCIE
Z390	PCIE GbE	PCIE	PCIE SATA 0A	PCIE GbE SATA 1A	PCIE GbE SATA 0B	PCIE SATA 1B	PCIE SATA 2	PCIE SATA 3	PCIE SATA 4	PCIE SATA 5	PCIE	PCIE
Q370	PCIE GbE	PCIE	PCIE SATA 0A	PCIE GbE SATA 1A	PCIE GbE SATA 0B	PCIE SATA 1B	PCIE SATA 2	PCIE SATA 3	PCIE SATA 4	PCIE SATA 5	PCIE	PCIE





PCH		
A2	VSS	AL12
A28	VSS	AL17
A3	VSS	AL21
A33	VSS	AL24
A37	VSS	AL26
A4	VSS	AL29
A45	VSS	AL33
A46	VSS	AL38
A47	VSS	AM1
A48	VSS	AM18
A5	VSS	AM32
A8	VSS	AM49
AA19	VSS	AN12
AA20	VSS	AN16
AA25	VSS	AN34
AA27	VSS	AN38
AA28	VSS	AP4
AA30	VSS	AP46
AA31	VSS	AR12
AA49	VSS	AR16
AA5	VSS	AR34
AB19	VSS	AR38
AB25	VSS	AT1
AB31	VSS	AT16
AC12	VSS	AT18
AC17	VSS	AT21
AC33	VSS	AT24
AC38	VSS	AT26
AC4	VSS	AT29
AC46	VSS	AT32
AD1	VSS	AT34
AD19	VSS	AT45
AD2	VSS	AV11
AD22	VSS	AV39
AD25	VSS	AW10
AD49	VSS	AW4
AE12	VSS	AW40
AE33	VSS	AW46
AE38	VSS	B47
AE4	VSS	B48
AE46	VSS	B49
AF22	VSS	J4
AF25	VSS	J40
AF28	VSS	BA12
AC1	VSS	BA14
AG22	VSS	J47
AG23	VSS	BA4
AG25	VSS	BA5
AG27	VSS	BA6
AG28	VSS	BB41
AG30	VSS	BB43
AG31	VSS	BB9
AG49	VSS	BC10
AH12	VSS	BC13
AH17	VSS	BC15
AH33	VSS	BC19
AH38	VSS	BC24
AH19	VSS	BC26
AJ20	VSS	BC31
AJ25	VSS	BC35
AJ27	VSS	BC40
AJ28	VSS	BC45
AJ30	VSS	BC8
AJ31	VSS	BD43
AK19	VSS	BE44
AK20	VSS	BF1
AK25	VSS	BF2
AK27	VSS	BF3
AK28	VSS	BF48
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		BG25
		BC28

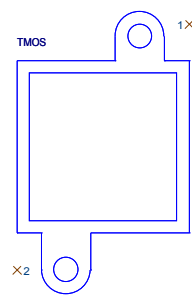
9 of 13

Z390MP/BO/S

PCH		
BG3	VSS	M24
BG33	VSS	M32
BG37	VSS	M34
BG4	VSS	M49
BG48	VSS	N5
C12	VSS	N12
C25	VSS	N16
C30	VSS	N34
C4	VSS	N35
C48	VSS	N37
C5	VSS	N38
D12	VSS	P26
D16	VSS	P29
D17	VSS	P4
D30	VSS	P46
D33	VSS	R12
D8	VSS	R16
E10	VSS	R26
E13	VSS	R29
E15	VSS	R3
E17	VSS	R34
E19	VSS	R38
E22	VSS	R4
E24	VSS	T17
E26	VSS	T18
E31	VSS	T32
E33	VSS	T4
E35	VSS	T49
E40	VSS	T5
E42	VSS	T7
E8	VSS	U12
F41	VSS	U15
F43	VSS	U17
F47	VSS	U21
G44	VSS	U24
G6	VSS	U33
H8	VSS	U38
J10	VSS	V20
J26	VSS	V22
J29	VSS	V4
J49	VSS	V46
J40	VSS	W25
J46	VSS	W27
J47	VSS	W28
J48	VSS	W30
J49	VSS	Y10
J9	VSS	Y12
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M16	VSS	Y38
M18	VSS	Y9
M21	VSS	

12 of 13

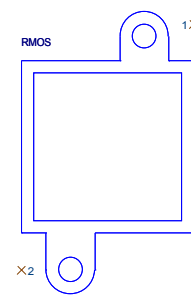
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X2

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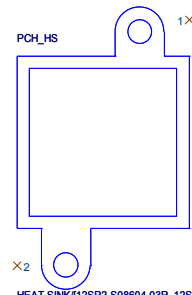
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1.5mm thermal pad



X2

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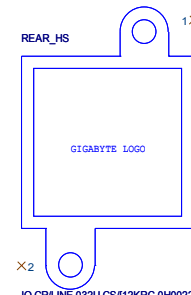
Footprint : same Z370 HD3
MOSH5INK-Z370_HD3P-R
PN: 12SP2-S08026-J1R/J2R/J3R
1.5mm thermal pad



X2

HEAT SINK[12SP2-S08604-03R_12SP2-S08604-01R_12SP2-S08604-02R]

Footprint : same H370 HD3
BGAHSINK-Z370_HD3P
PN: 12SP2-S08604-03R/01R/02R



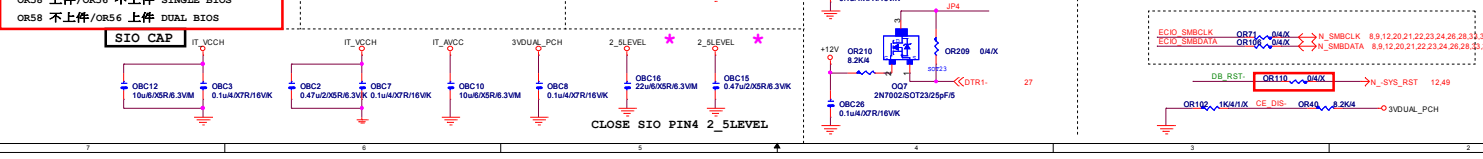
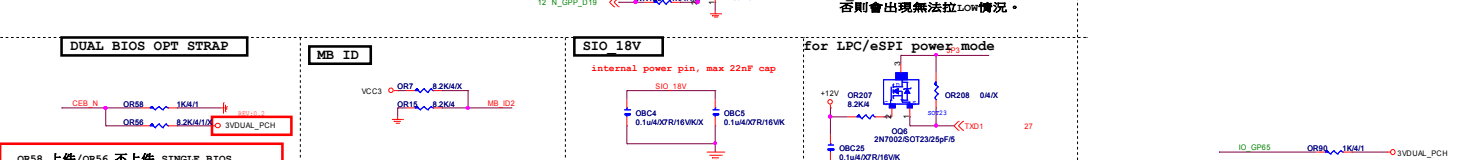
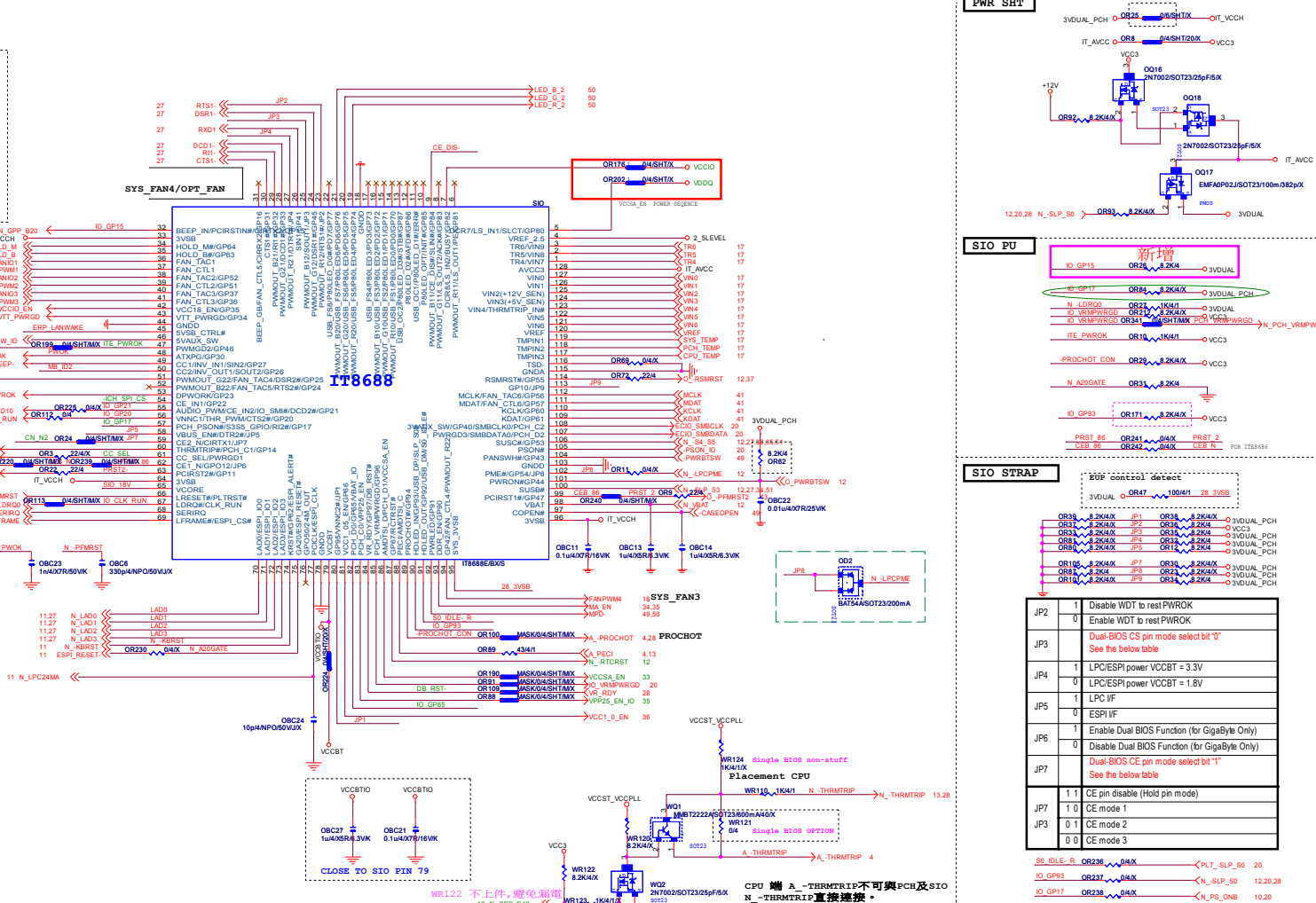
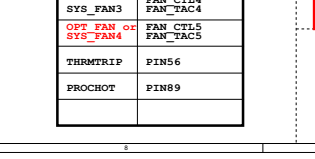
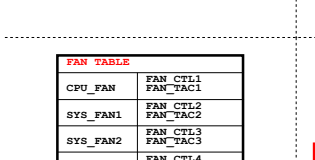
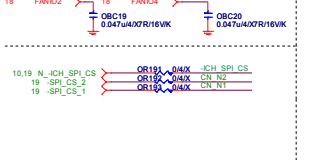
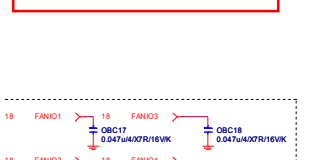
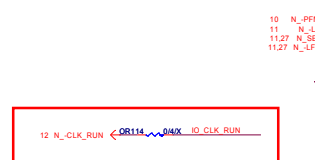
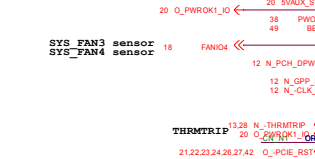
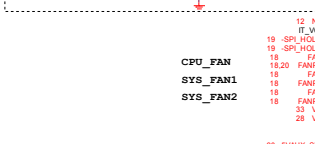
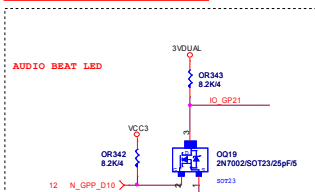
X2

IO CRLINE 032U CS[12KRC-0H0022-01R]X

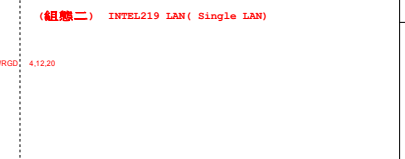
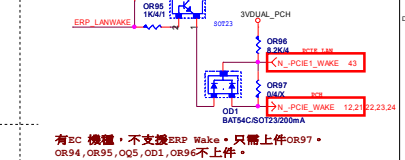
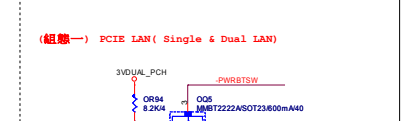
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Z390_AORUS_G7_IO_COVER
PN: 12KRC-0H0022-01R

CPL SIO IT8688 REV:0.7

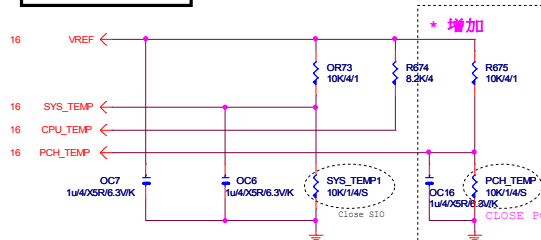
IT8688 COMA+LED



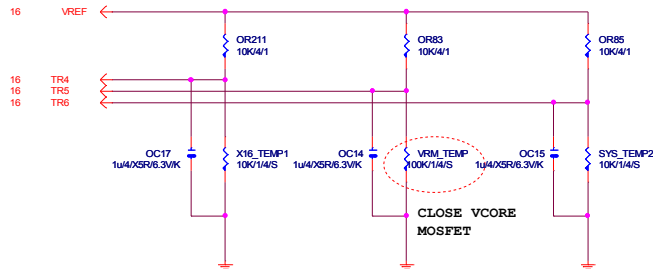
請依開來規格，選擇Support Exp下 LAN Wake up組態。



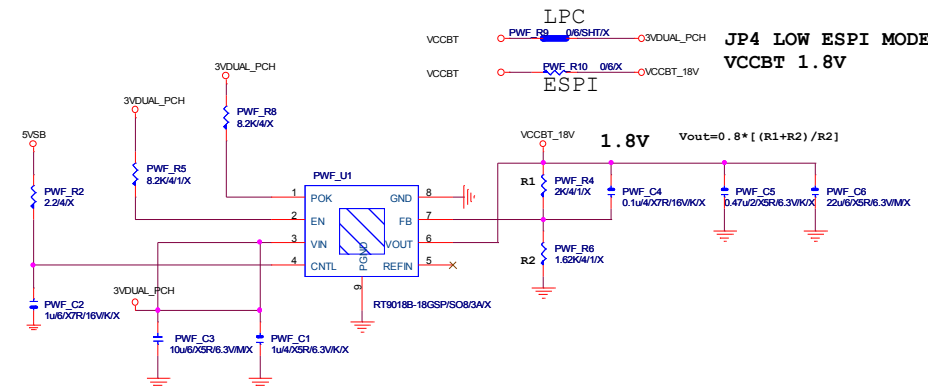
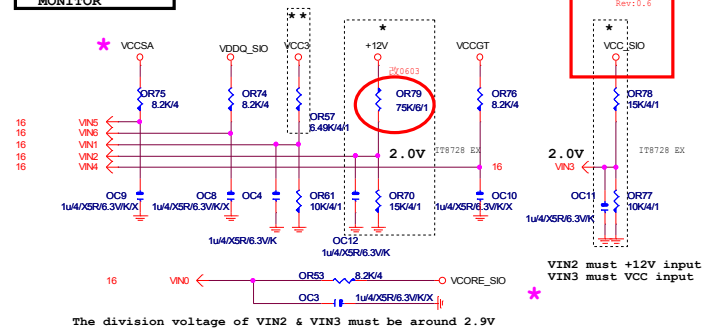
TEMP H/W MONITOR



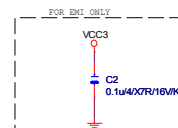
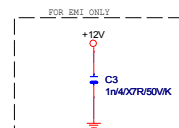
5個FAN時使用



VOLTAGE-- H/W MONITOR



(靠近ATX CONNECTOR)

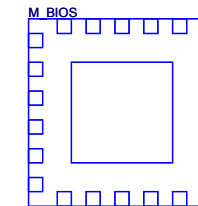
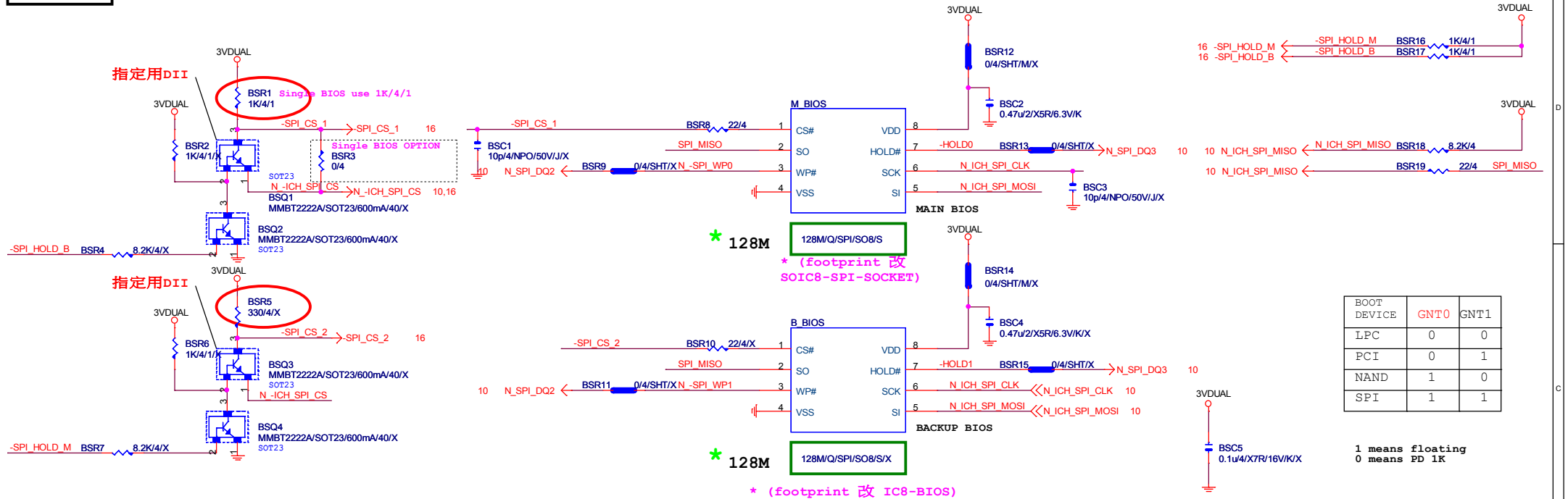


★Update 2015-04.24

Gigabyte Technology

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DUAL BIOS



LCP/G-FL/1.27mm/200MIL/WHITE[10SL2-000008-31R]/X

* 試產先上，PVT 移除

IT8686

GIGABYTE™

Title

CEC relate circuit

Size

Document Number

Custom

390 UD

Date:

Monday, March 30, 2020

Sheet

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of

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Rev

1.15

The diagram illustrates the CEC (Control Electronics Circuit) relate circuit for the IT8686 chip. It shows the internal connections of the IT8686 chip (CECU1) to various external components and signals. Key components include resistors (CECR1-CECR39), capacitors (CECC1-CECC6), and transistors (CECQ3-CECQ5, CECQD2). Signals include N_PS_ONB, N_SLP_S0, N_PCH_VRMPWRGD, N_SMBCLK, N_SMBDATA, N_GPP_B3, FANPWM1, PLT_SLP_S0, O_PWROK1, SVAUX_SW, and PSON. The diagram also shows the ATX Power Discharge section at the top, which includes a +12V input and several resistors (CECR5-CECR7) and transistors (CECQ3-CECQ5). The IT8686 chip is shown with its pins and internal connections, including the CECU1 block and various signal lines. The diagram is labeled with various component values and pin numbers, and includes a title block at the bottom right with the GIGABYTE logo and document information.

PCIE*4

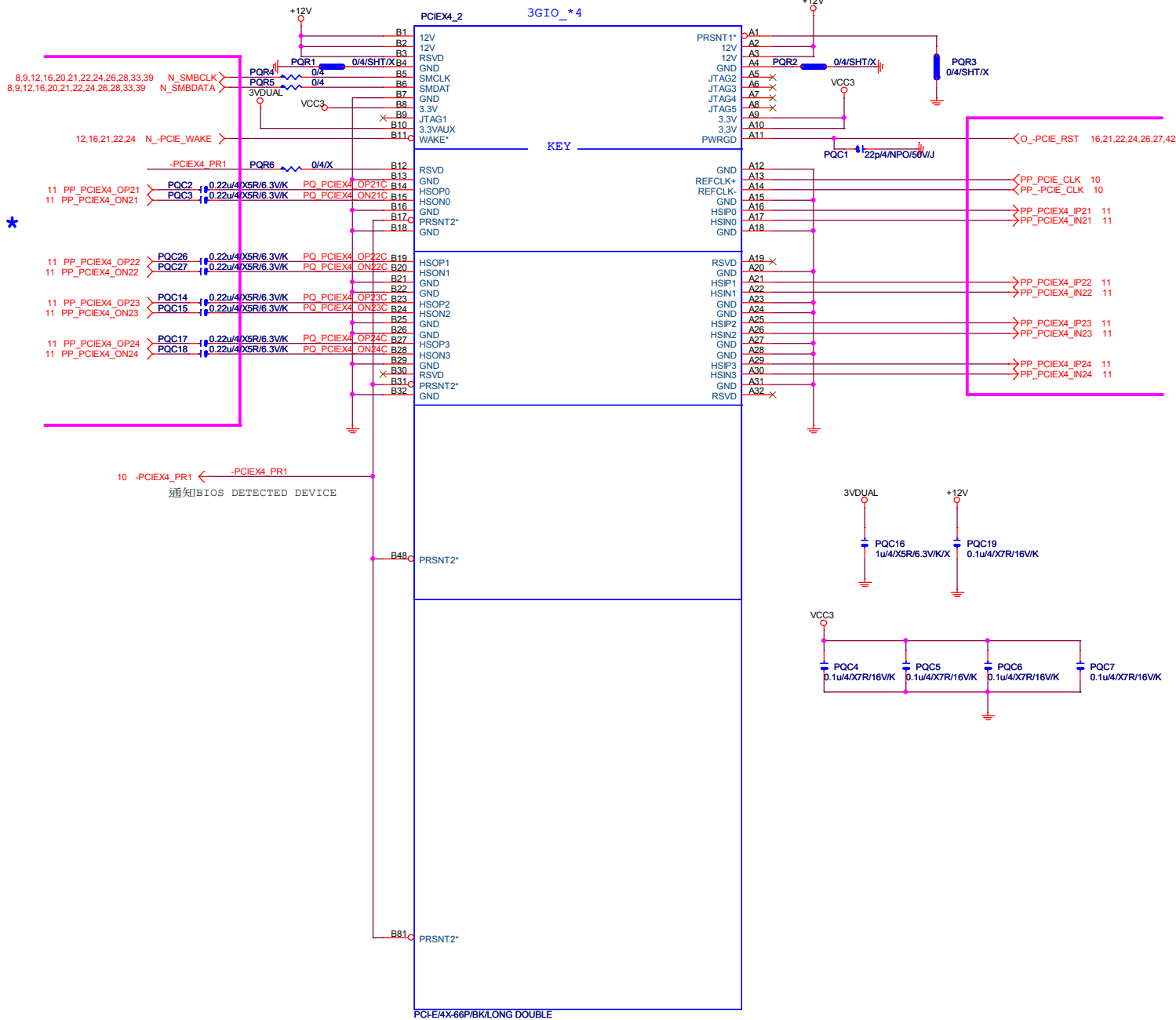


11AC1-023065-17R: 黑色

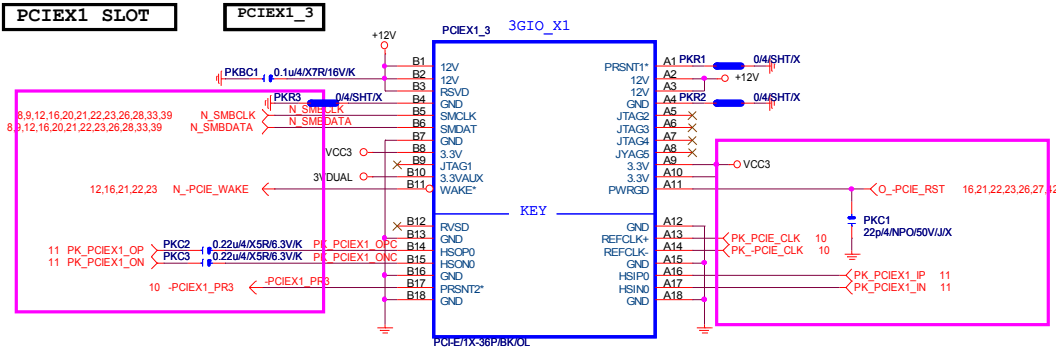
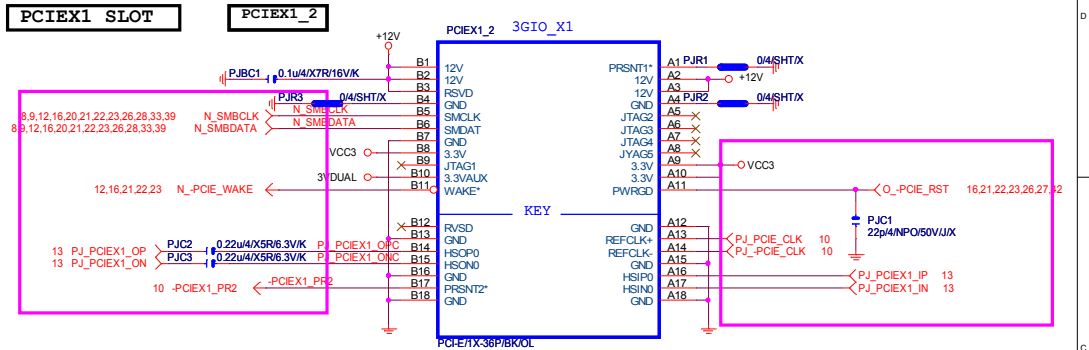
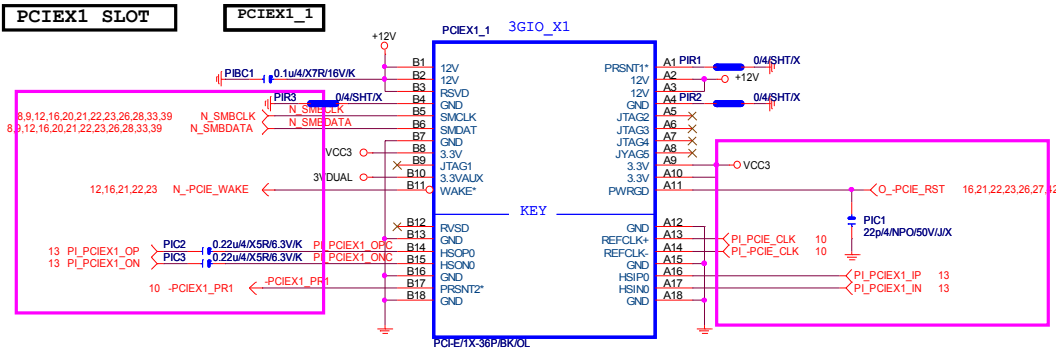
Rev 0.51

PCIE*4

* Footprint "PCIESLOT-64P-1"

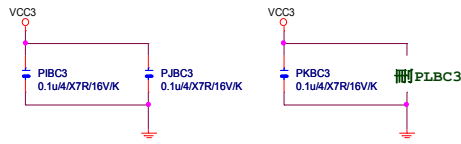


11A1-023065-17R:黑色



删除PCIE1_4

PCIE1 SLOT PCIE1_4



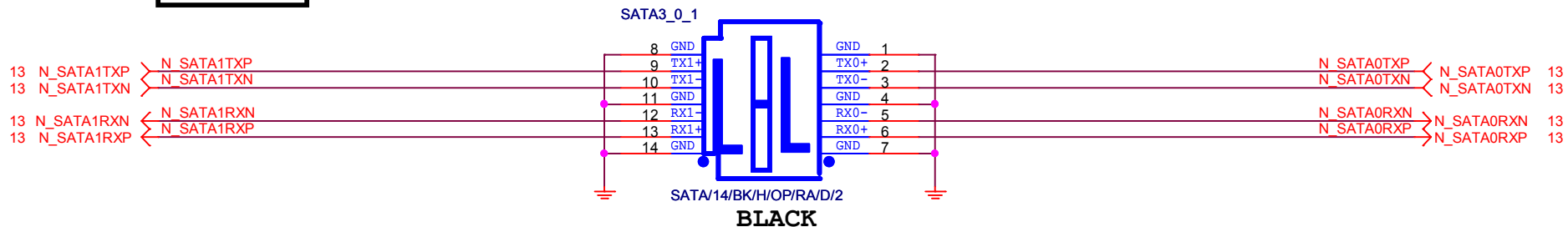
IO18/IO19 To SATA3 port0/1 (90度R-A, 180度V-A)

上 Port (8~14)

下 Port (1~7)

6 SATA3 from Z390 (90度R-A)

SATA3 0/1



Footprint : H2X7-SATA2-D90

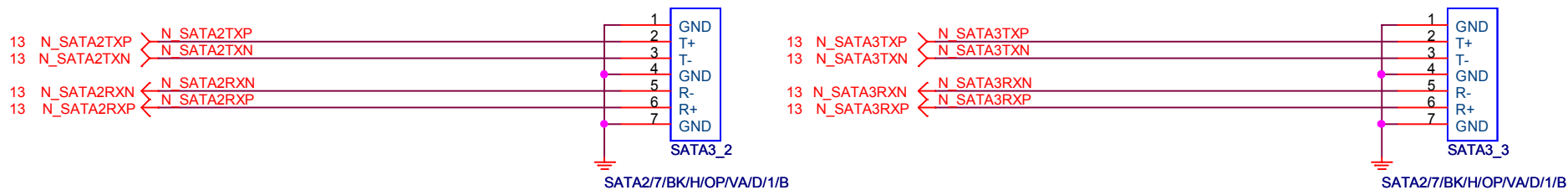
IO20/IO21 To SATA3 port2/3

上 Port (8~14)

下 Port (1~7)

teknisi-indonesia.com

SATA3 2/3



Footprint : H1X7-SATA2-HS-MASK

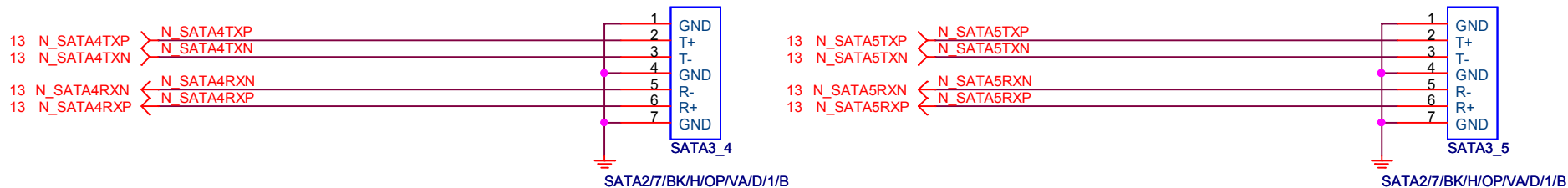
Footprint : H1X7-SATA2-HS-MASK

IO22/IO23 To SATA3 port4/5

上 Port (8~14)

下 Port (1~7)

SATA3 4/5



Footprint : H1X7-SATA2-HS-MASK

Footprint : H1X7-SATA2-HS-MASK

Gigabyte Technology

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SATA		
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REVERSED

VCC3

M2AR5
1K4/I

VCC3

M2AR6
1K4/I

-M2A_DETECT

M2ASSD_FDS

M2AR4

04/SHT/10A/X

N_GPP_K0

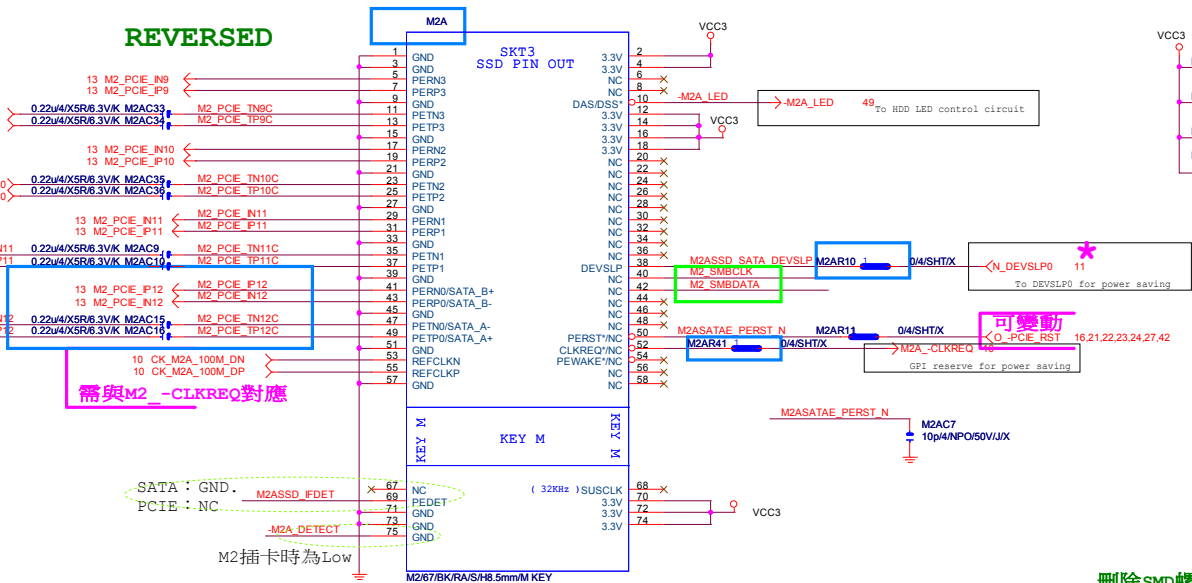
M2A₁₃

M2AR1

04/SHT/10A/X

N_GPP_K1

M2A₁₂



若M2A 32G有架高
原本的DIP螺柱太矮了,更新為
CR/[12KSF-F10303-11R]

DIP螺柱

80

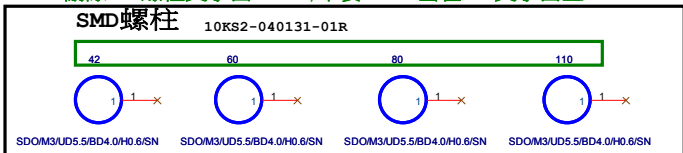
CR/[12KSF-F10303-11R]

DIP螺絲

80

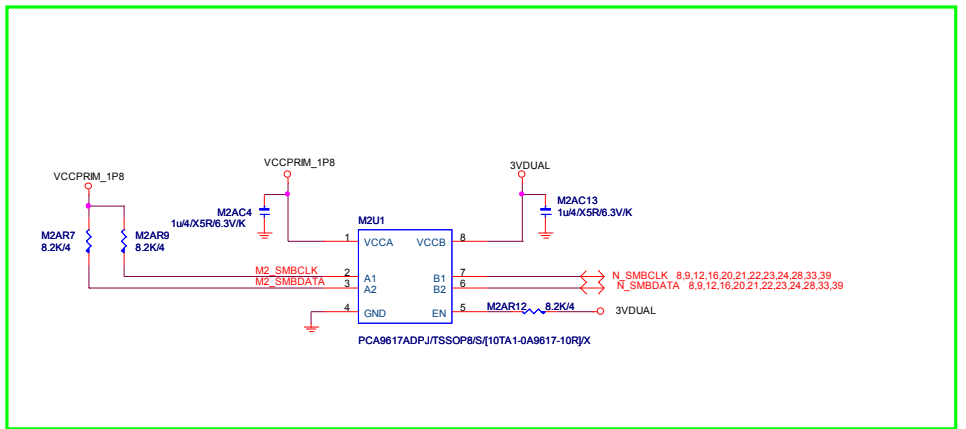
CR/[12KS2-110202-01R]

架高



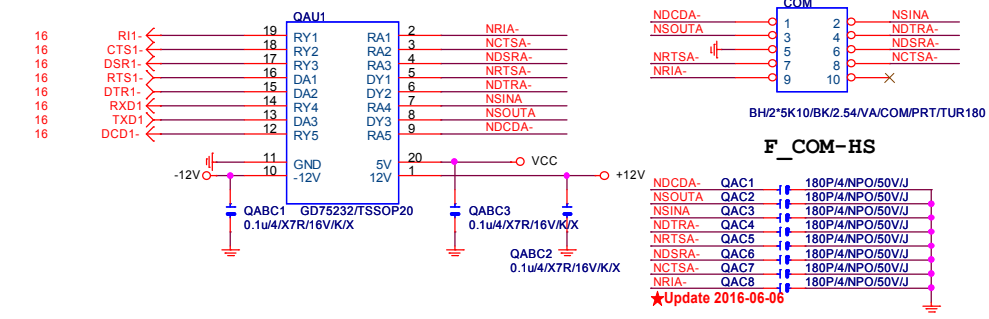
* Footprint : HOLE_C236D165-A

Flex IO priority	N_GPP_K1	IO14 PCIe#9	IO15 PCIe#10	IO16 PCIe#11	IO17 PCIe#12
M2A SATA	L	PCIE	PCIE	SATA 0	SATA 1
M2A PCIe (PCIe Reverse)	H	PCIE	PCIE	PCIE	PCIE



COM PORT

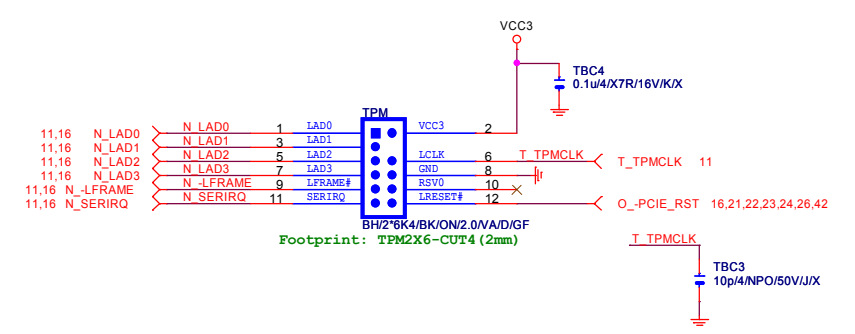
Rev: 0.91



LPT PORT

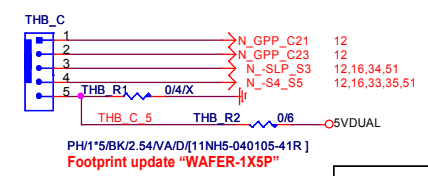
模組為：IT8686 LPT+COMA，單獨刪除LPT...CHHECK.

TPM CONNECT

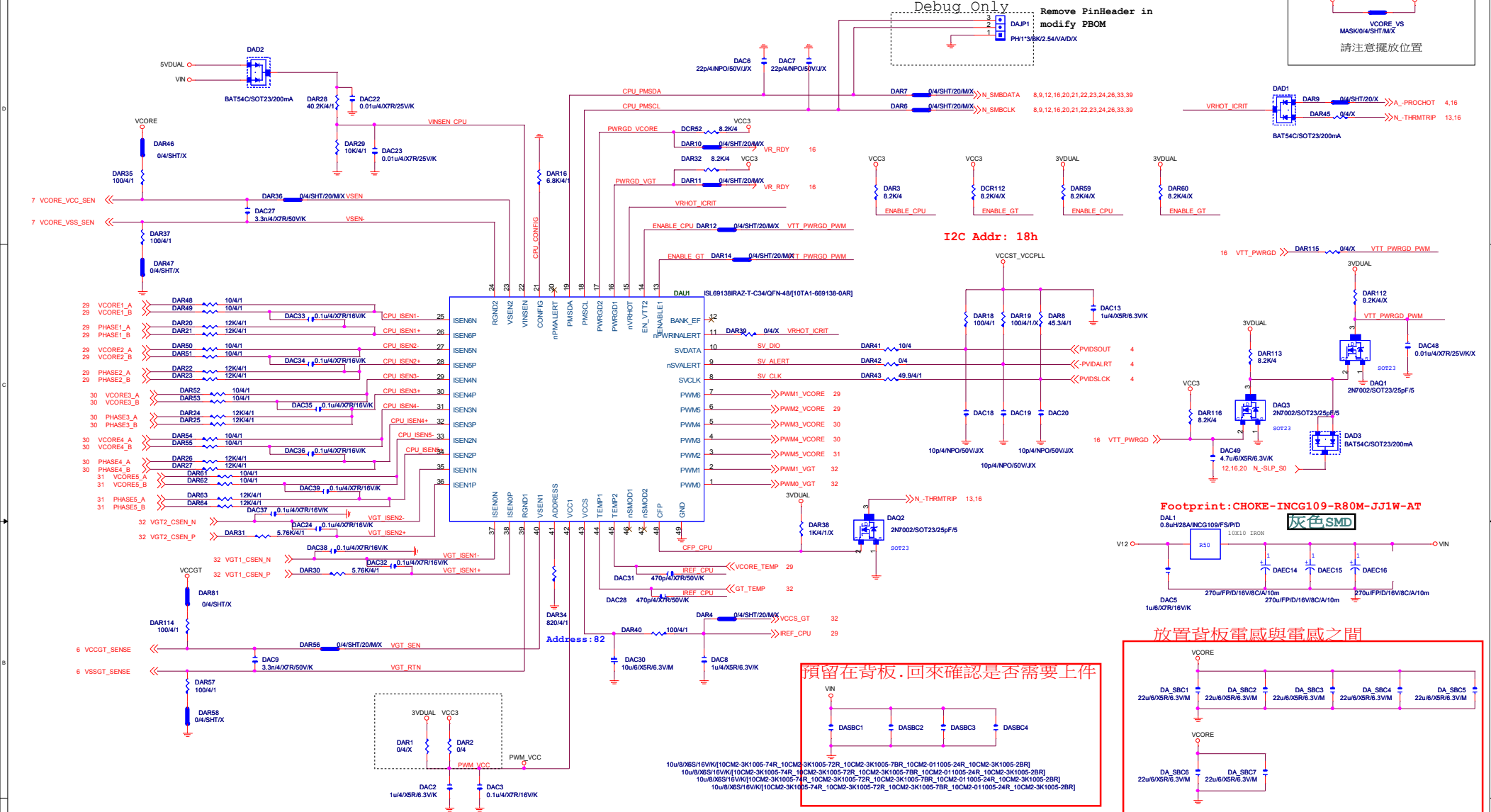
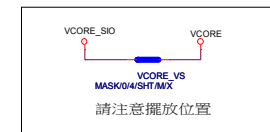


Thunderbolt

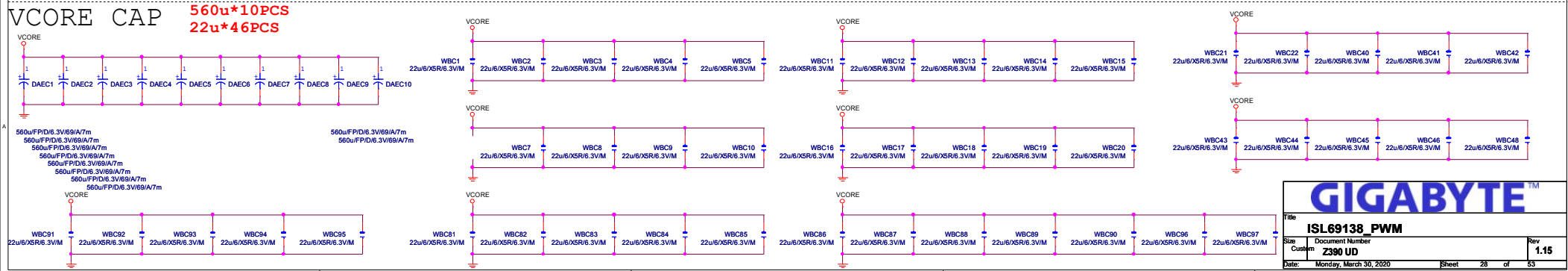
★Update 2015-12-29



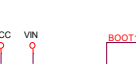
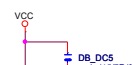
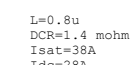
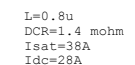
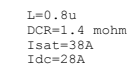
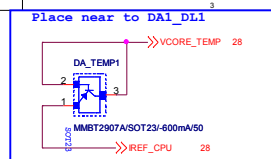
Gigabyte Technology			
Title			
COM, TPM ,THB			
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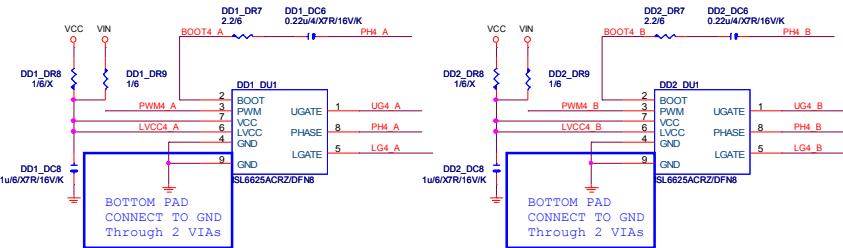
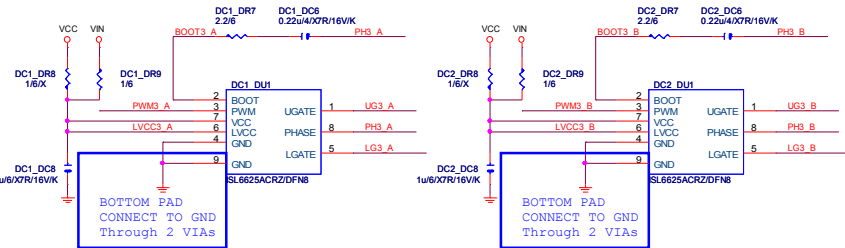
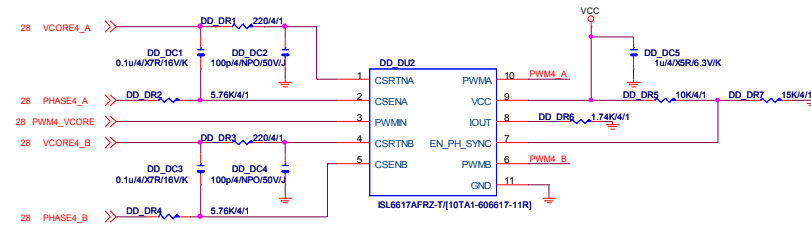
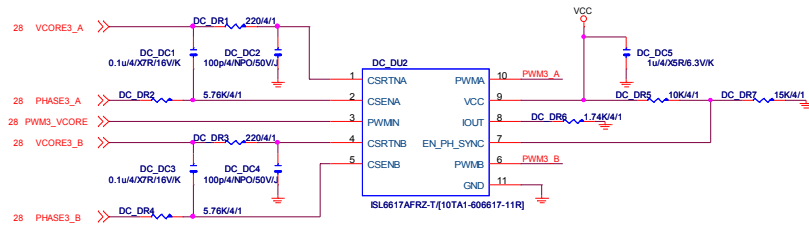
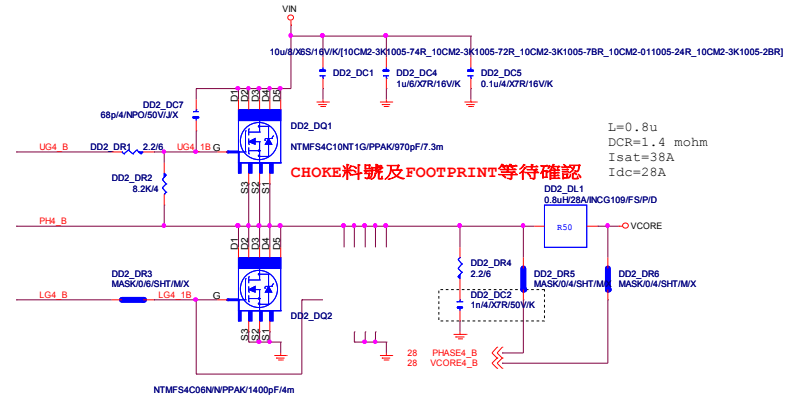
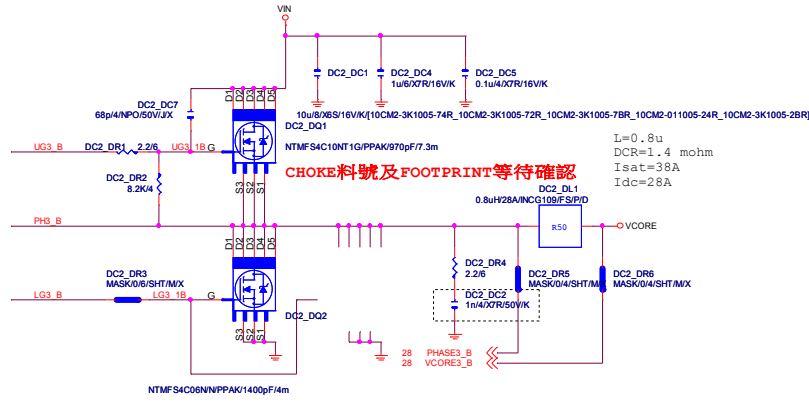
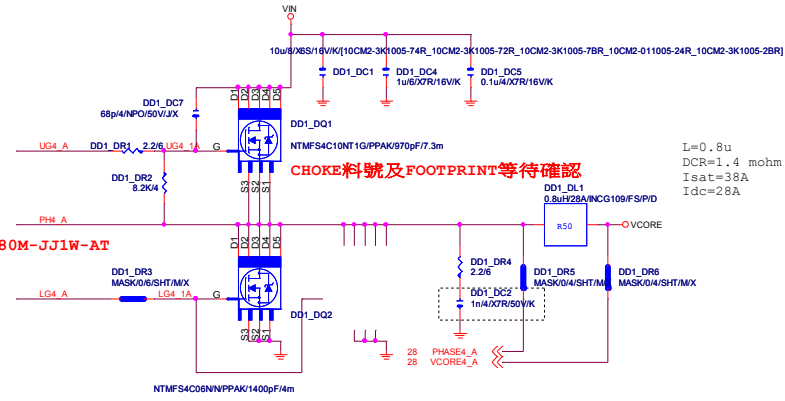
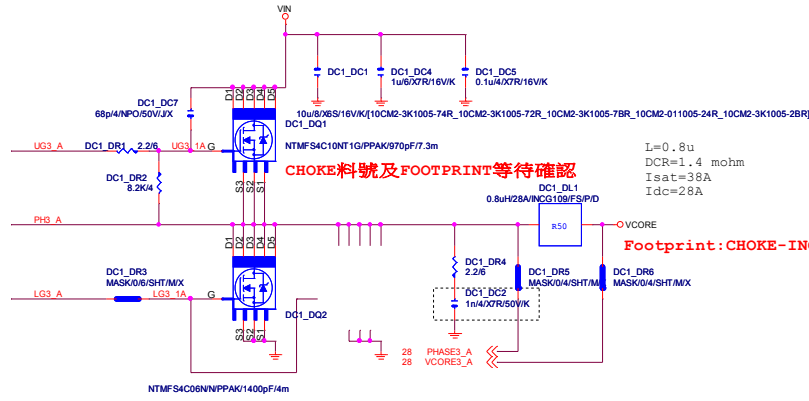
VCORE CAP 560u*10PCS
22u*46PCS



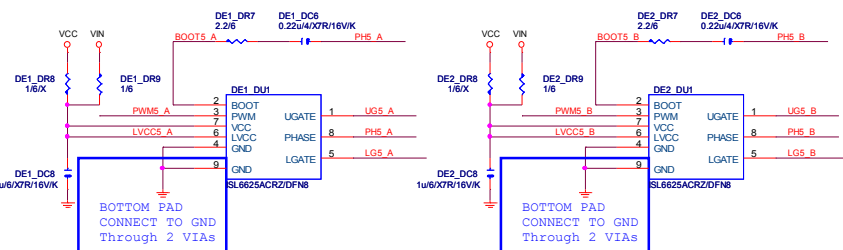
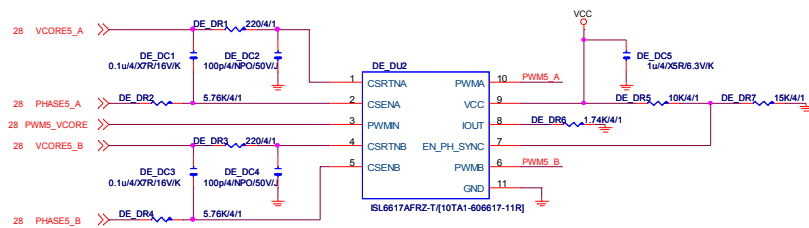
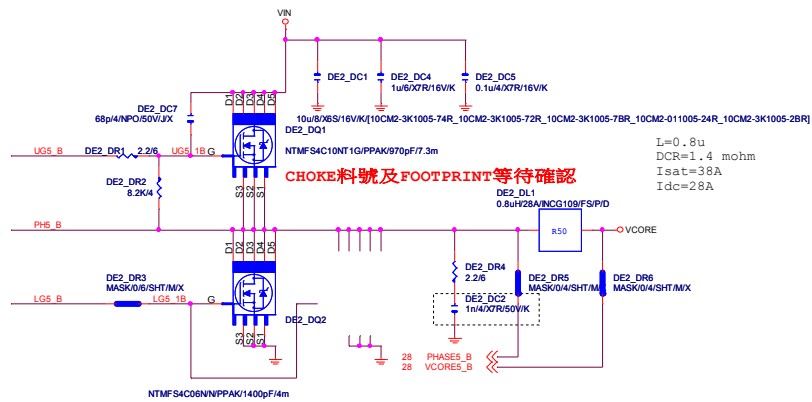
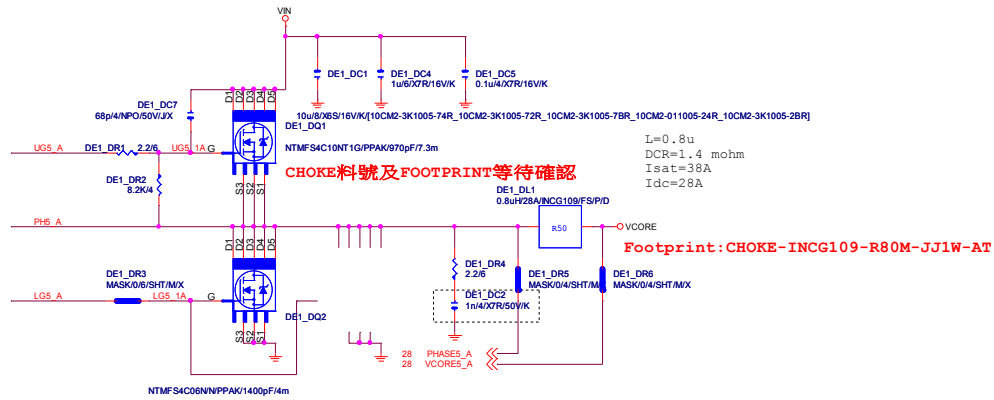
10u8/X6S/16V/K/10CM2-3K1005-74R 10CM2-3K1005-72R 10CM2-3K1005-7BR 10CM2-011005-24R 10CM2-3K1005-2BR



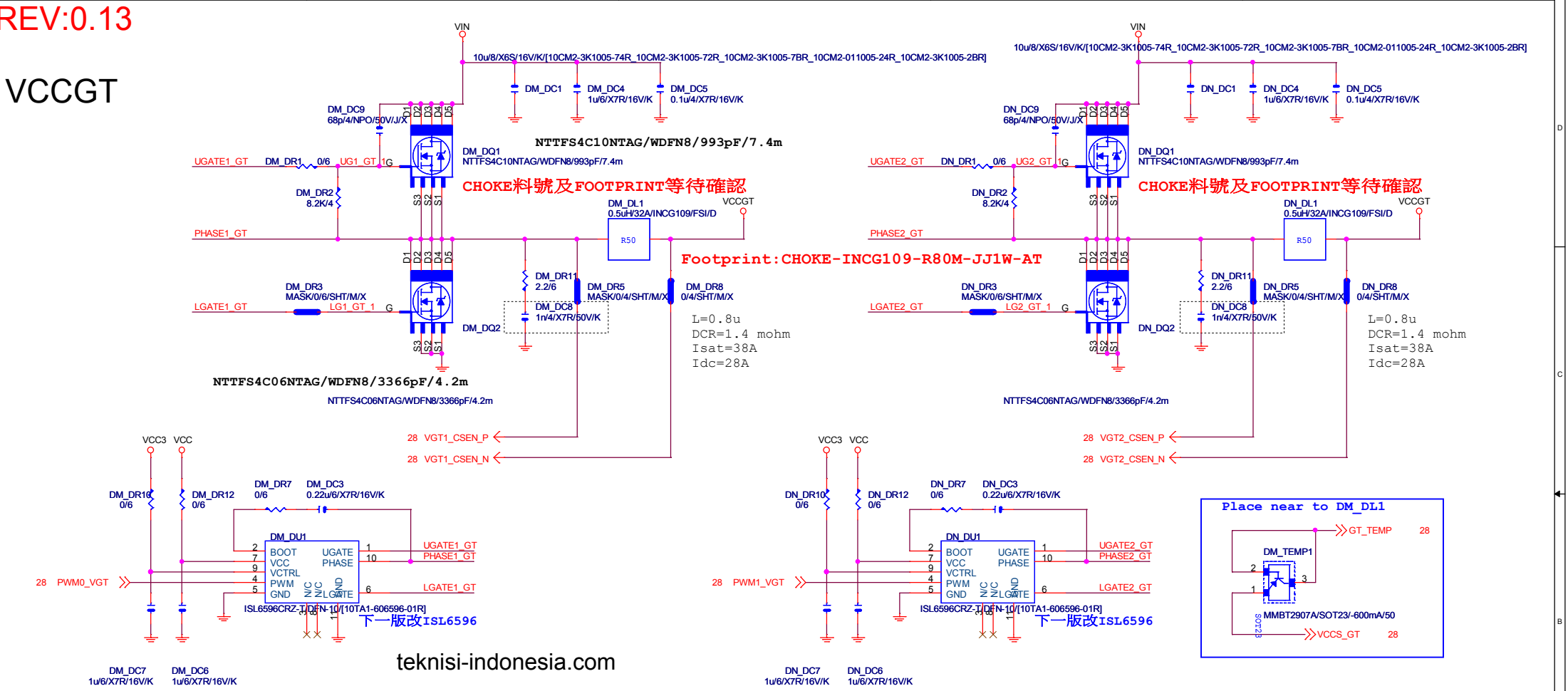
REV:0.13



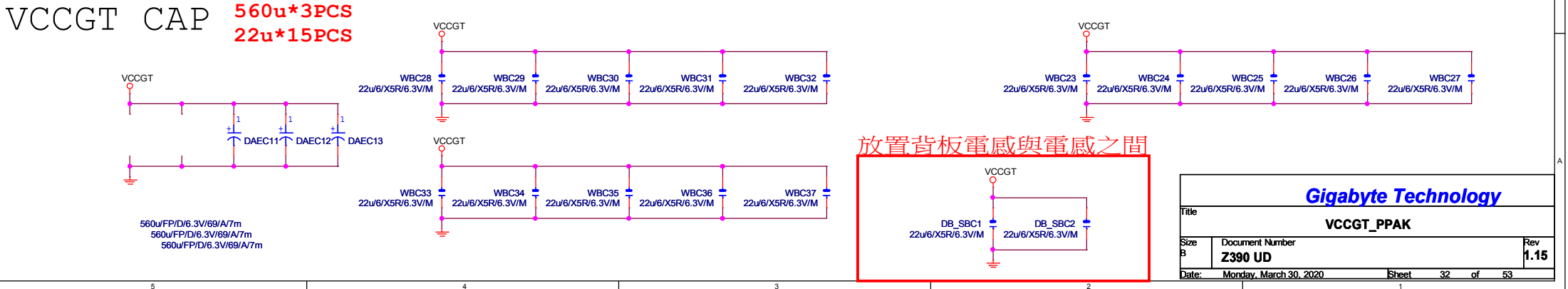
REV:0.13



VCCGT

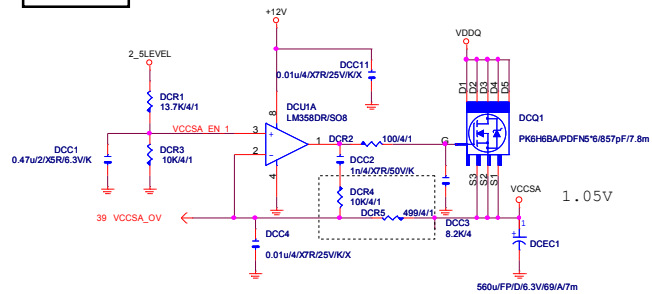


VCCGT CAP 560u*3PCS
22u*15PCS



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Title		
VCCGT_PPAK		
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VCCSA

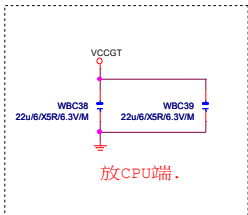


Connect to IT8793

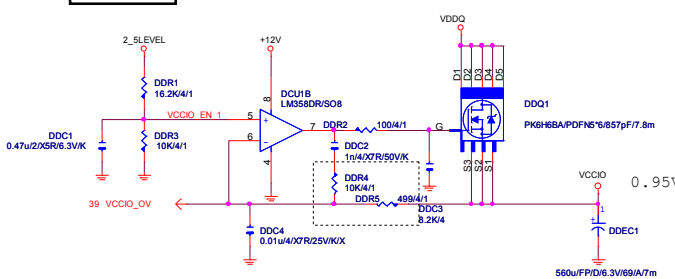
Connect to IT8686

SIO PIN5 . PIN7 用在其他function時
DCQ2 . DCR6 . DCQ3 . DCQ4 DCR7 . DCR8 . DCC7 上件
DDR7 不需要預留

SIO PIN5 . PIN7接VDDQ . VCCIO時
DCQ2 . DCR6 . DCQ3 . DCQ4 DCR7 . DCR8 . DCC7 不上件
DDR7 可以SHORT PAD



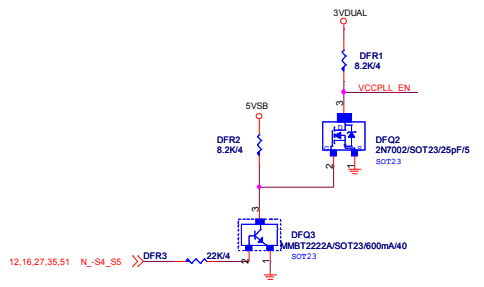
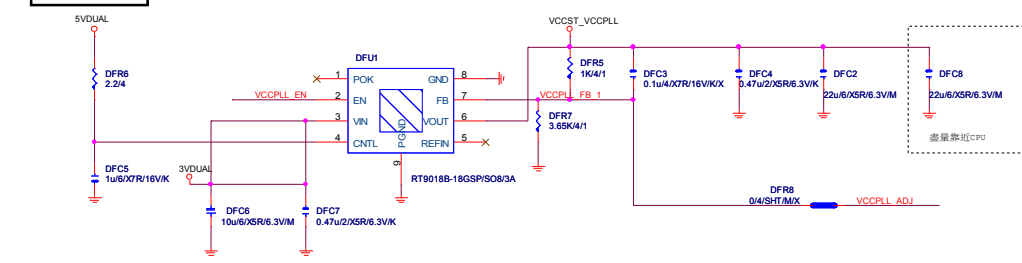
VCCIO



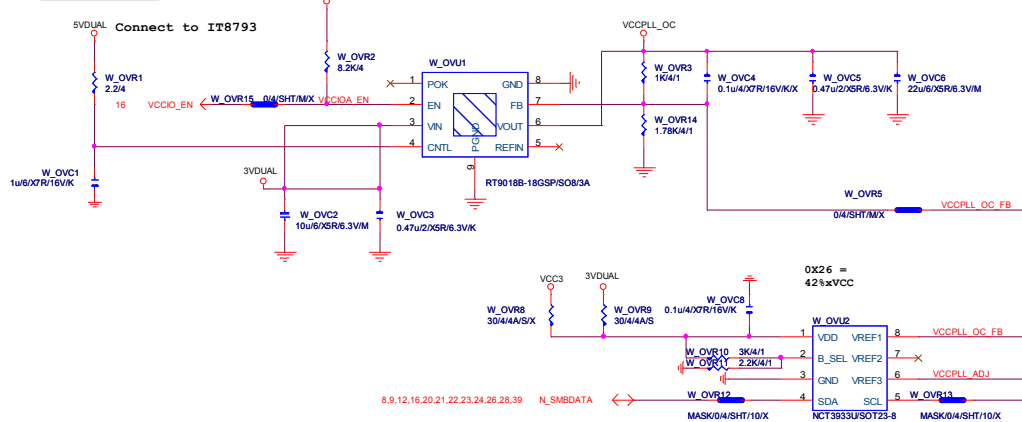
Connect to PCH

Connect to IT8686

VCCST_VCCPLL



VCCPLL_OC






For power sequence require

1.1A MAX

指定fingerprint "EC6D8MM-RH-2"

* 大電容 x0

			
Title			
RT8120_DDR4 POWER			
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REV:0.11

VPP 25V

SMD Molding(合金)
10LC4-15100B-01R CORE 1.0uH 15A
TAI-TECH SMD TMPA0603S-1R0MN-D
DCR=6.7m

L=1u
DCR=6.7 mohm
Isat=15A
Idc=12A

CHOKE-合金

4. VPP_25V CHOKE footprint 改CHOKE6X6mm_SMD-1

CHOKE與CAP料號可變

DDR_VPP VIN CAP
560u*1PCS

$V_{(BR)DSS}$	$R_{DS(on) MAX}$	$I_D MAX$
30 V	4.2 mΩ @ 10 V	67 A
	6.1 mΩ @ 4.5 V	

L=1u
DCR=6.7 mohm
Isat=15A
Idc=12A

SUPPORT DDR4 2.5V

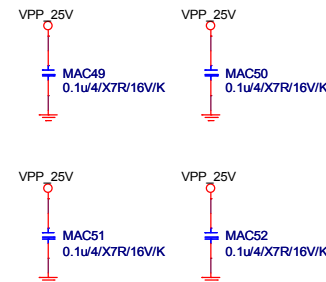
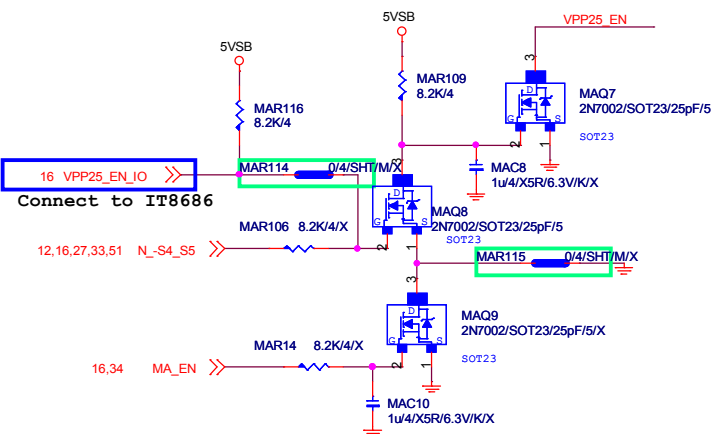
25A MAX

請放置CHOKE一出來位置.先預留.
請自行確認ripple後再決定是否上件

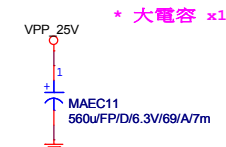
Remote sense請從最重的負載端點拉回

PWR SEQ

* 刪 MA_DR32



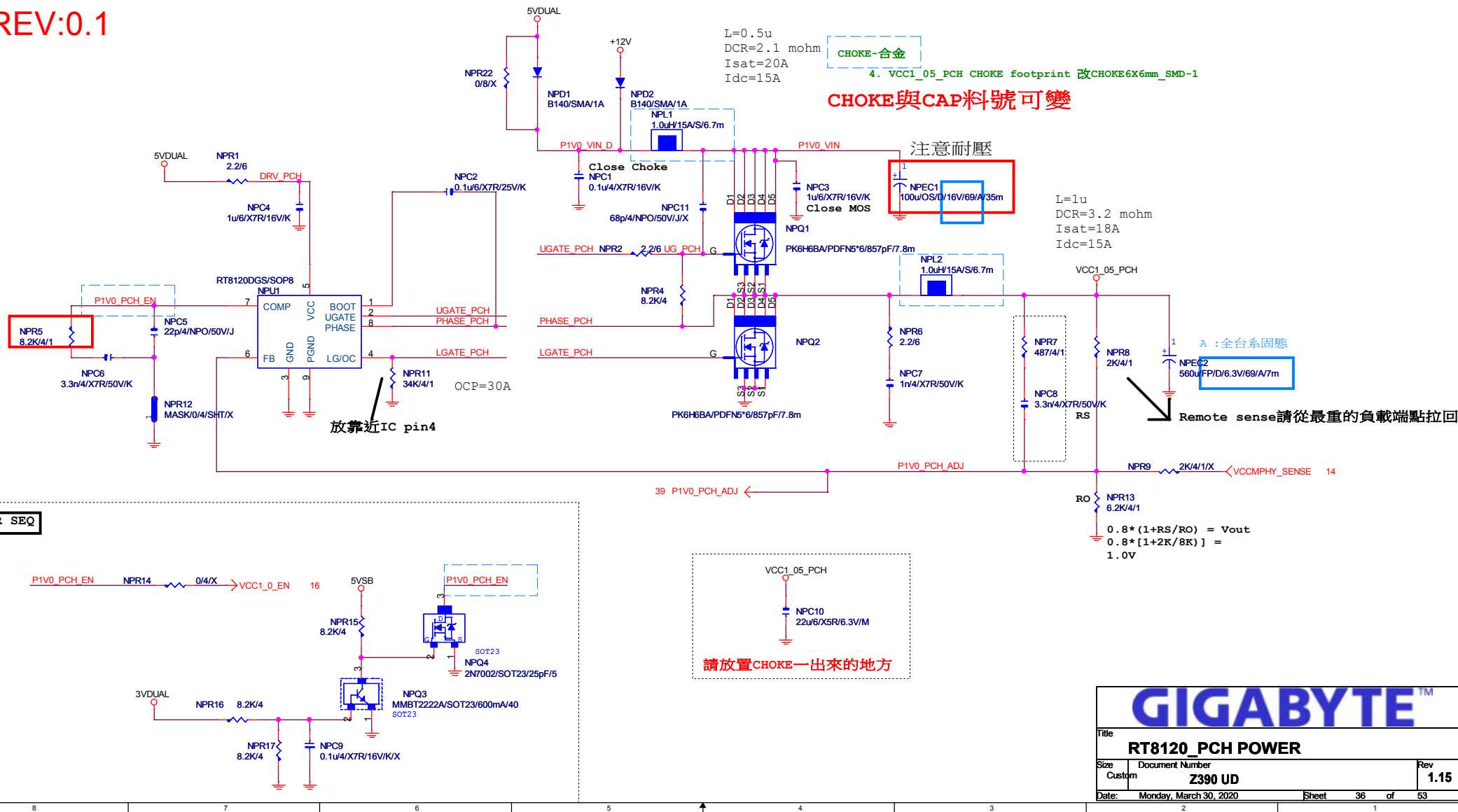
VPP CAP 560u*1PCS



GIGABYTE™

Title RT8120_VPP25 POWER		
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REV:0.1

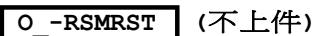


REV: 0.12

20 5VAUX SW



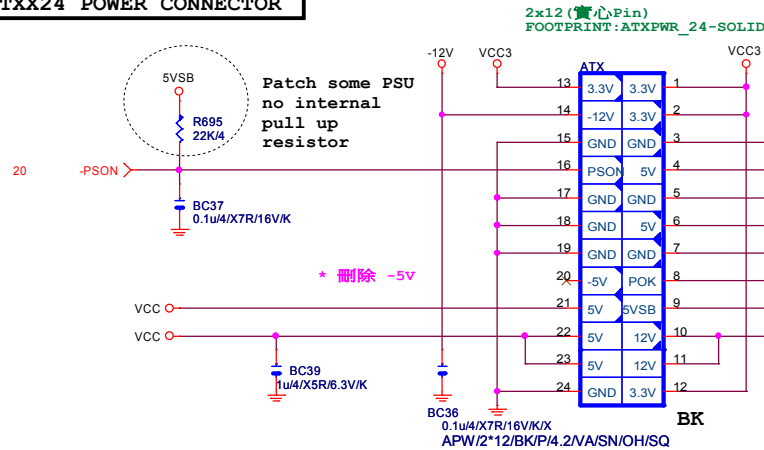
3VDUAL PCH



Gigabyte Technology

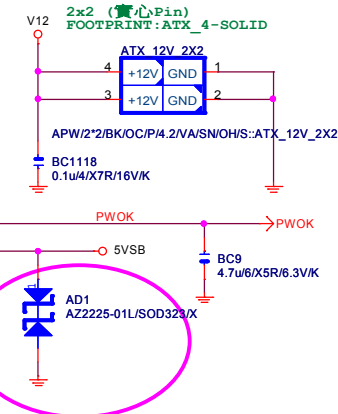
Title			
DISCRETE POWER			
Size	Document Number	Rev	
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ATXX24 POWER CONNECTOR

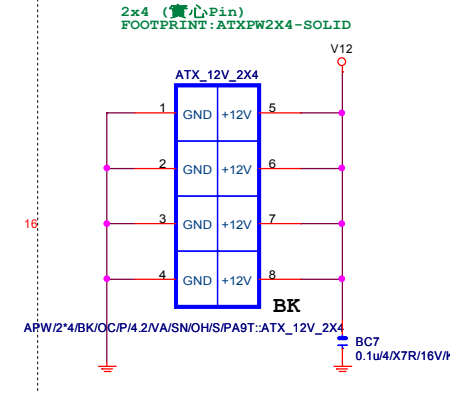


To prevent the 5VSB under loading when boot

11NH4-020004-P1R/P2R '實心4P耐高溫+鍍錫料號
11NH4-020108-21R/22R '實心8P耐高溫+鍍錫料號
11NH4-020024-21R/22R '實心24P耐高溫+鍍錫料號

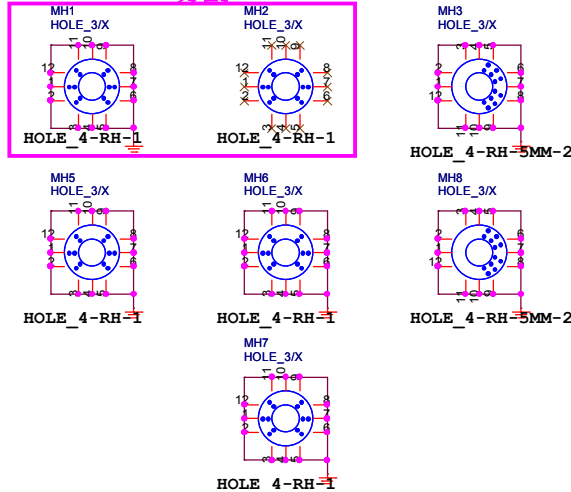


ATXX4 POWER CONNECTOR

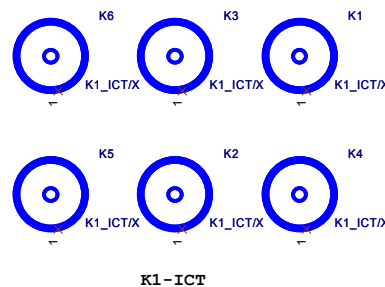


螺絲孔

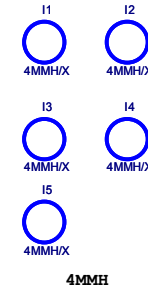
FOR AUDIO 切割



固定孔/光學點

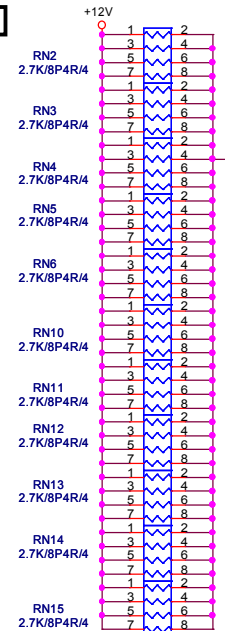


To prevent the 5VSB under loading when boot

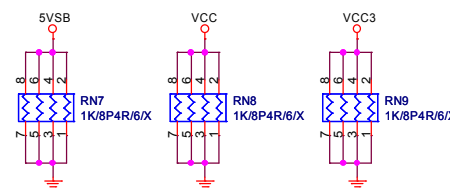


+12V DUMMY LOAD

To fix 12V light load abnormal issue

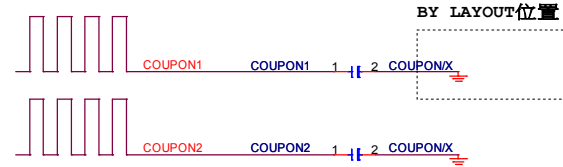


DUMMY LOAD



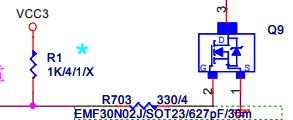
-PROHOT

COUPON



BY LAYOUT位置

Check R1 是否會漏電



Gigabyte Technology

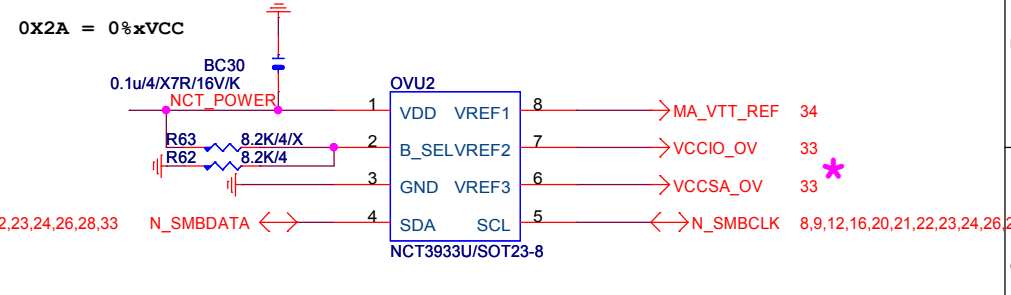
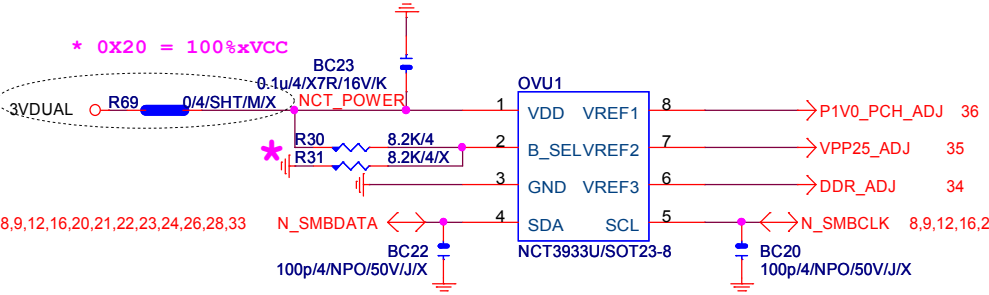
Title: **ATX POWER CONNECTOR**

Size: Custom Document Number

Rev: **1.15**

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OVER VOLTAGE



0X22 = 75%xVCC

* 删除 ovu3

NCT3933	0X2A	0X20	0X22
VREF1	DDRVT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

Gigabyte Technology

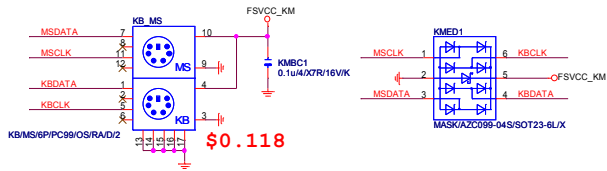
Title: **CPU CORE VR-2 NCT3933**

Size Custom	Document Number	Rev
	Z390 UD	1.15

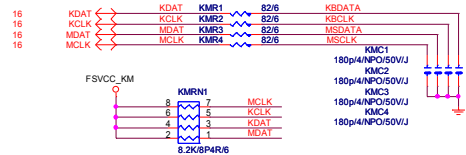
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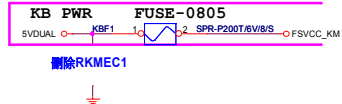
KB/MS



\$0.118



NET 可變, 與其他USB SHARE



刪除RKMEC1

Rev: 0.8

R_USB30_1

USB 電容前後NET 可自行調整

FSVCC_US1

R_USB30_2

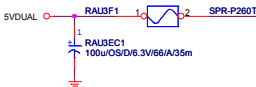
USB3.0/2.0

USB 電容前後NET 可自行調整

FSVCC_US1

★Update 2015-03.05

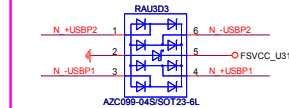
FUSE 2 Port 1 Fuse 2.6A



ESD 可自行SWAP PIN

ESD 可自行SWAP PIN

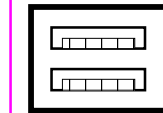
ESD 可自行SWAP PIN



CONNECTOR 自行調整

2 port USB 3.0 Capture:

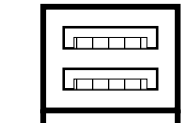
USB/18P/BU/OS/RA/D/2/1U/SB



Footprint:USB30_20

2 port USB 3.0 with TYPE C Capture:

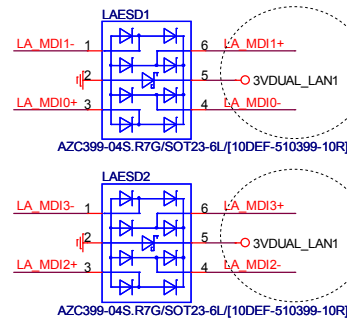
USB/18P/BU/OS/RA/D/2/1U/SB



Footprint:USB30_H-1

Gigabyte Technology

Rev 1.15

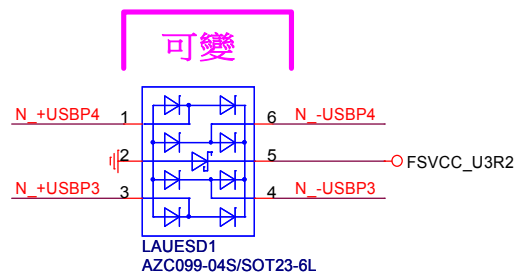
[illegible]

<p style="text-align: center;">Gigabyte Technology</p> <p style="text-align: center;">Realtek RTL8118</p>			
Title			
Size	Document Number		Rev
Custom	<p style="text-align: center;">Z390 UD</p>		1.15
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USB30_LAN CONNECTOR	R2.04
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RMA ESD PROTECT note:可變更USB NAME

RMA ESD PROTECT note:可變更USB NAME



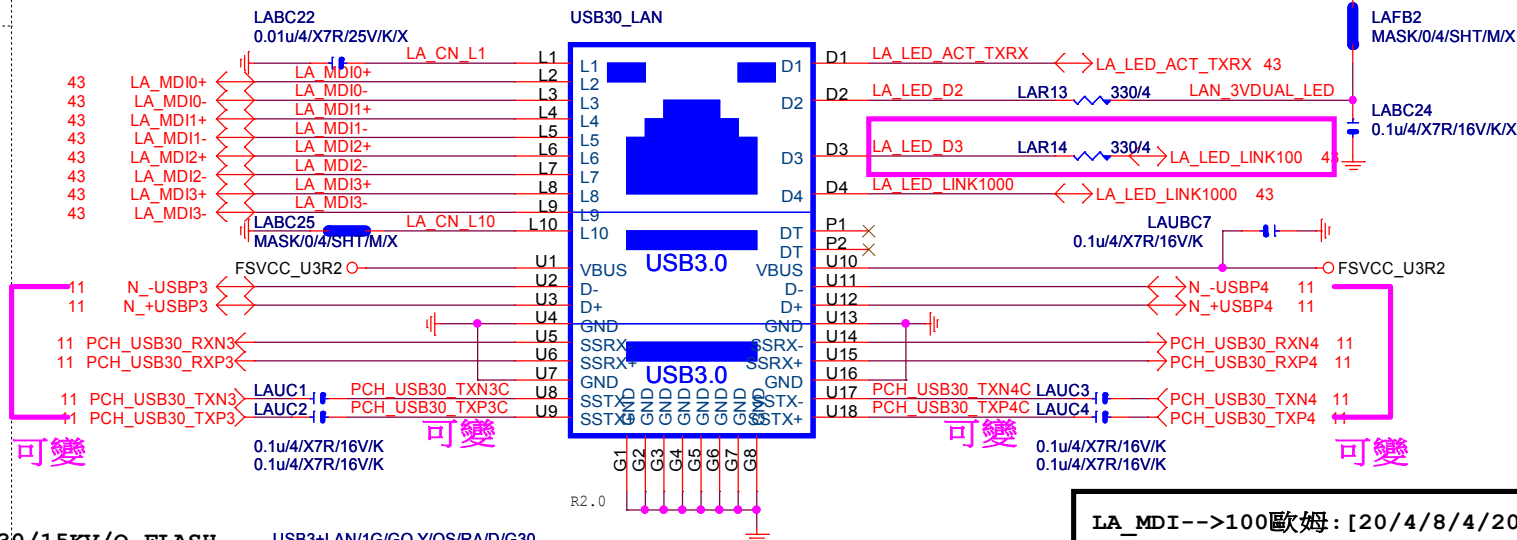
USB上白下藍:USB3+LAN/1G/GO,Y/OS/RA/D/G30/15KV/Q FLASH

USB3+LAN/1G/GO,Y/OS/RA/D/G30

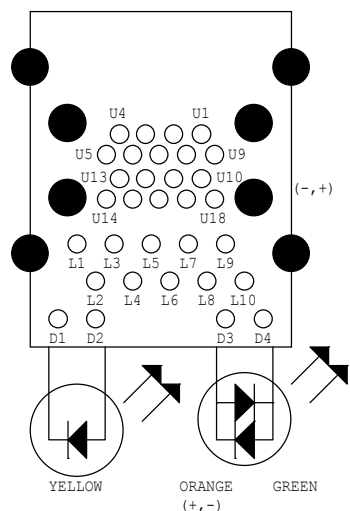
USB_LAN CONNECTOR

USB_LAN CONNECTOR

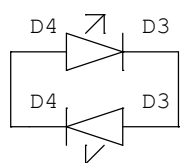
[RTL8118]



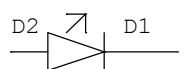
USB30_LAN LAYOUT示意圖



Dual Color LED

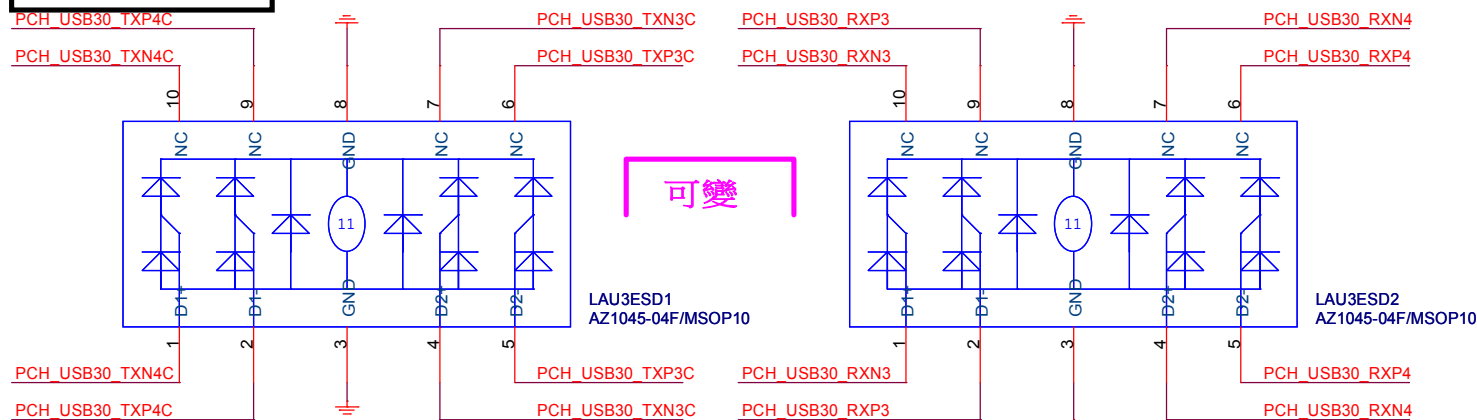


Single Color LED



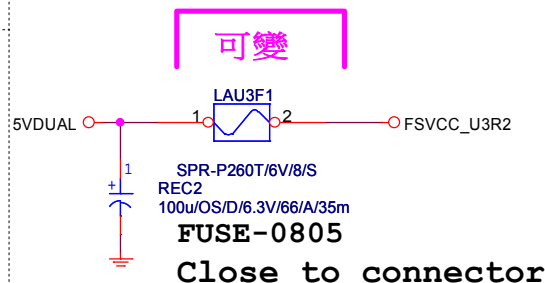
RMA ESD PROTECT

RMA ESD PROTECT



USB POWER

USB POWER



LAN POWER

LAN POWER



***ERP WOL**

Gigabyte Technology

Title **LAN CONNECTOR-RTL8118**

Size	Document Number
Custom	

Size	Document Number
Custom	

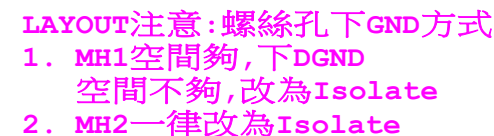
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Custom	

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50歐姆: 4/5



<input type="radio"/> MH1 DGND	<input type="radio"/> MH2 Isolate	
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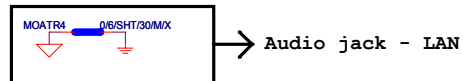
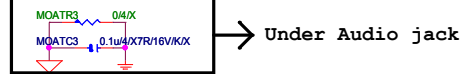
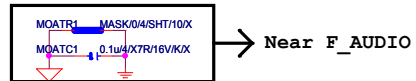
音效區域印刷



<div style="text-align: center;">  </div>			
<div style="text-align: center;"> Title HD AUDIO ALC887 </div>			
Size Custom	Document Number <div style="text-align: center; font-size: 1.5em;"> Z390 UD </div>		Rev <div style="text-align: center; font-size: 1.5em;"> 1.1 </div>
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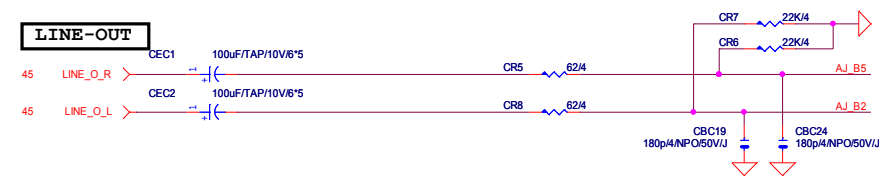
Rev 3.1

Rev 2.06

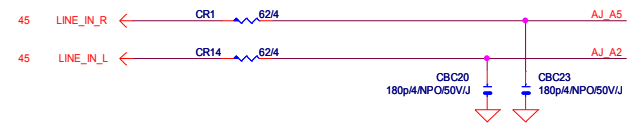


*量産前, 0ohm改short pad

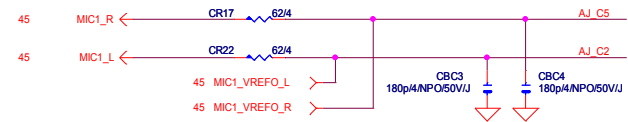
LINE-OUT



LINE-IN



MIC-IN



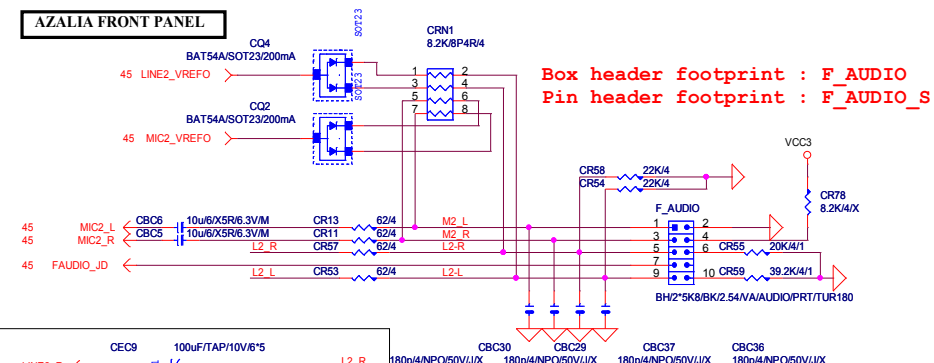
SURROUND

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CEN/LFE

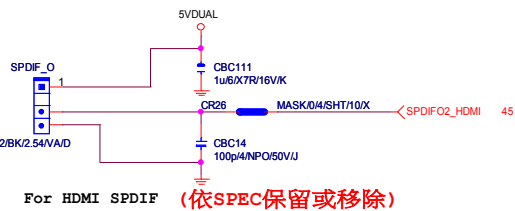
SURR BACK

AZALIA FRONT PANEL

Box header footprint : F_AUDIO
Pin header footprint : F_AUDIO_S

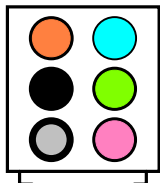
Gigabyte Technology

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AUDIO JACK		
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For HDMI SPDIF (依SPEC保留或移除)

AZALIA JACK



AZALIA JACK

BLUE

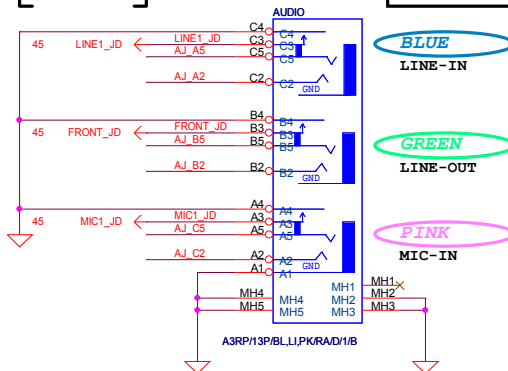
LINE-IN

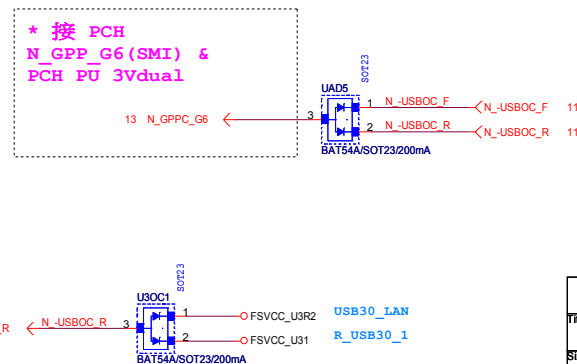
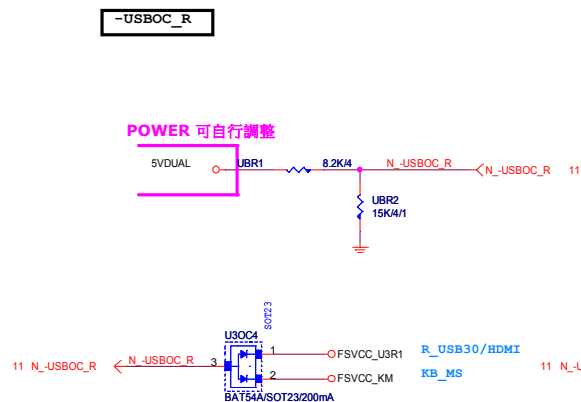
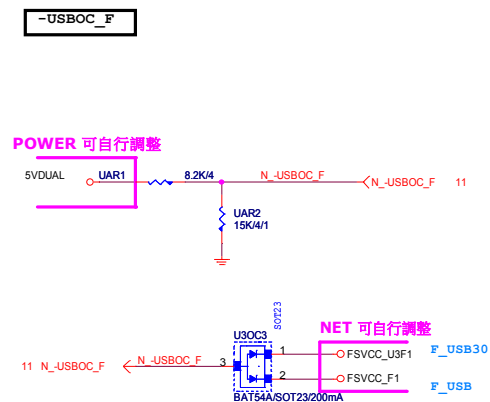
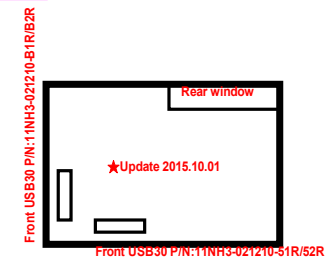
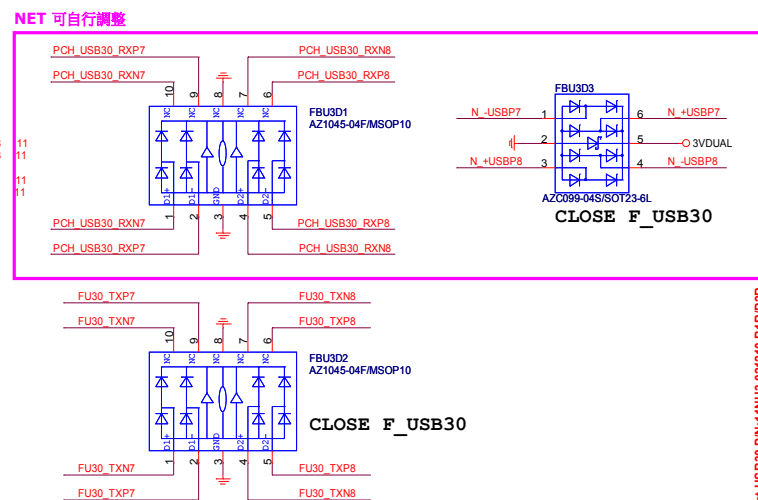
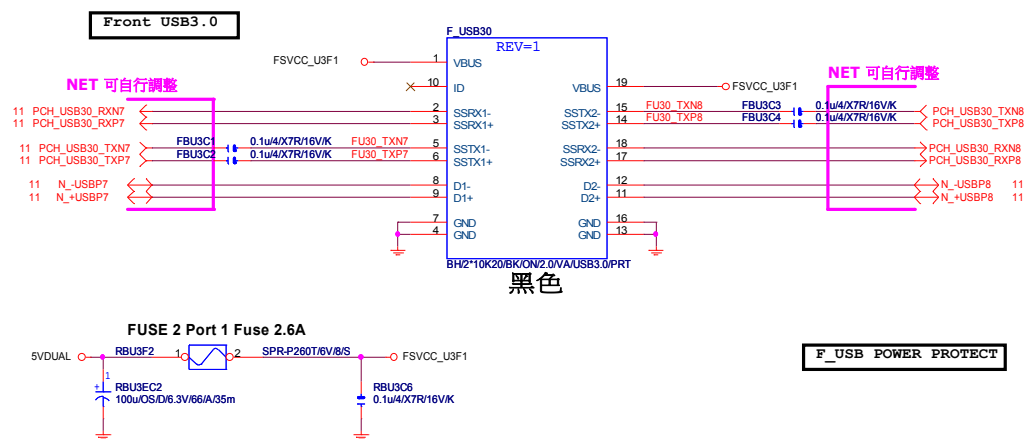
GREEN

LINE-OUT

PINK

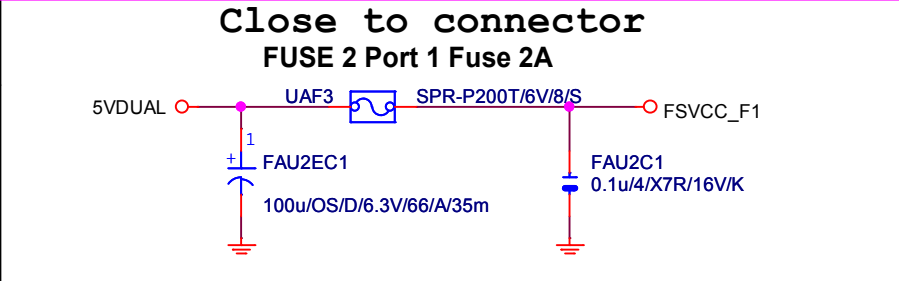
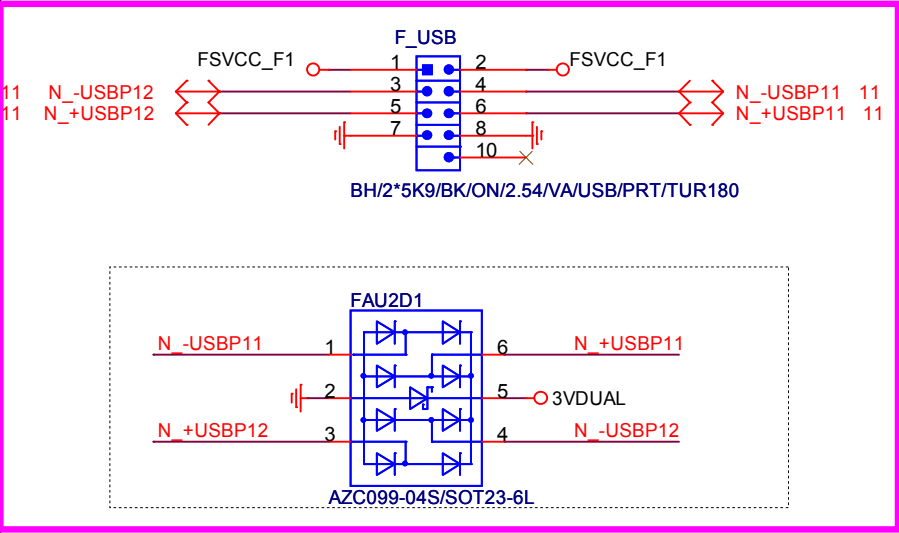
MIC-IN





NET 可變

FUSB2X5-HS



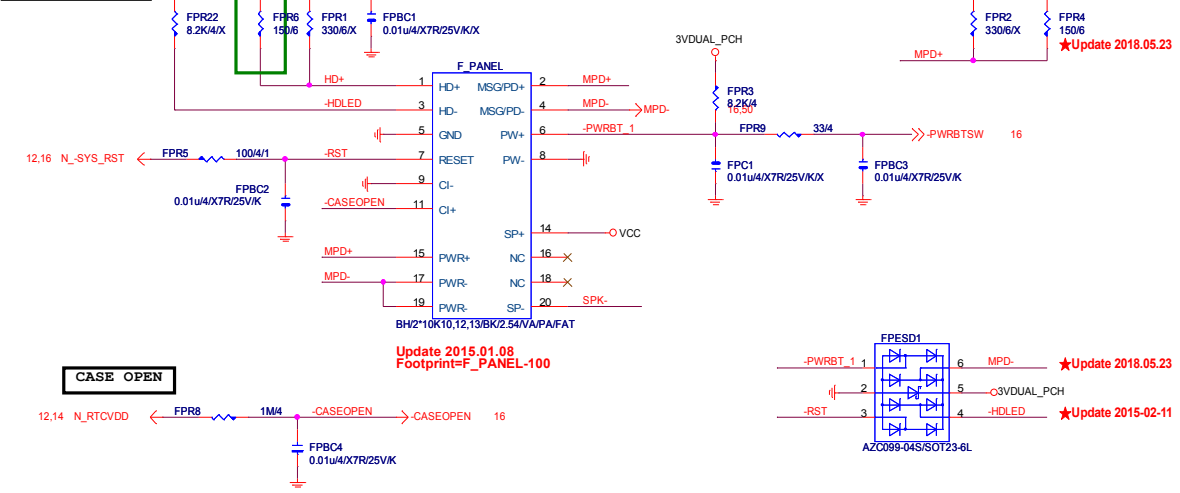
F_USB 2.0 OC SIGNAL

刪除U2OC1 ,移至與U3OC3接

刪除U2OC1 , FRONT USB2

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USB2.0			
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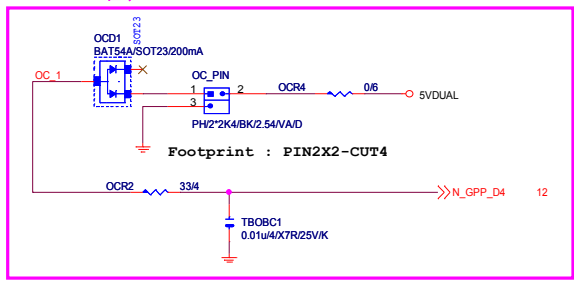
FRONT PANEL



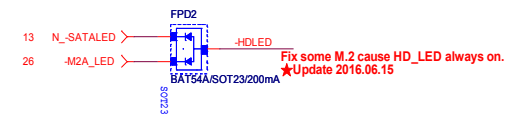
CASE OPEN

FRONT PANEL SHORT

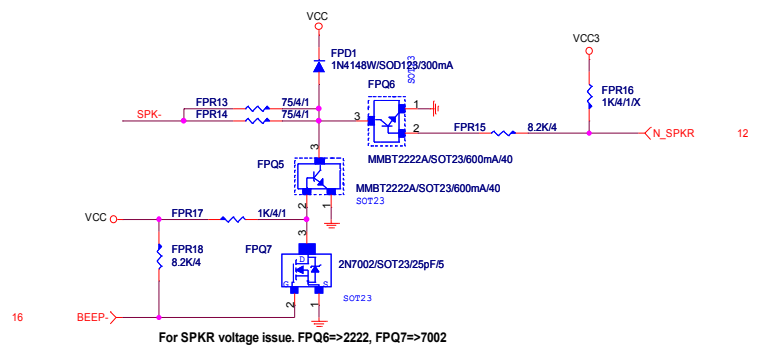
* FOR 客户Button

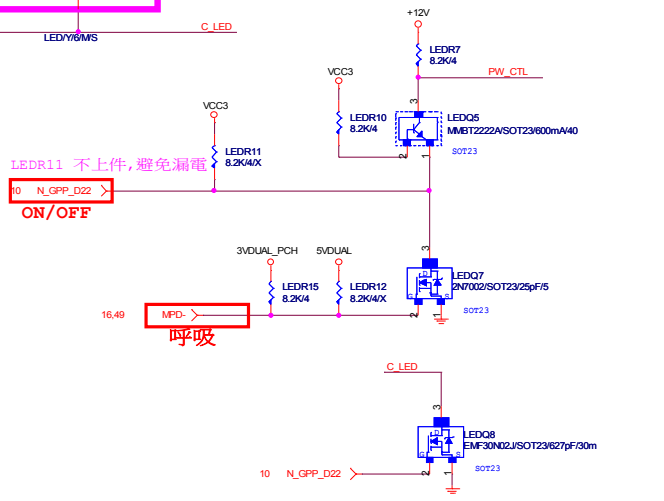
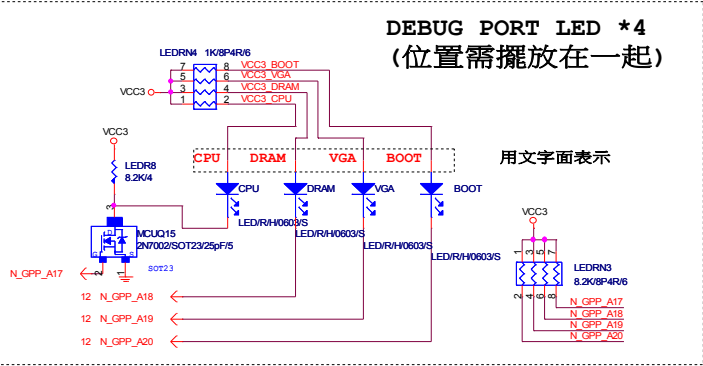
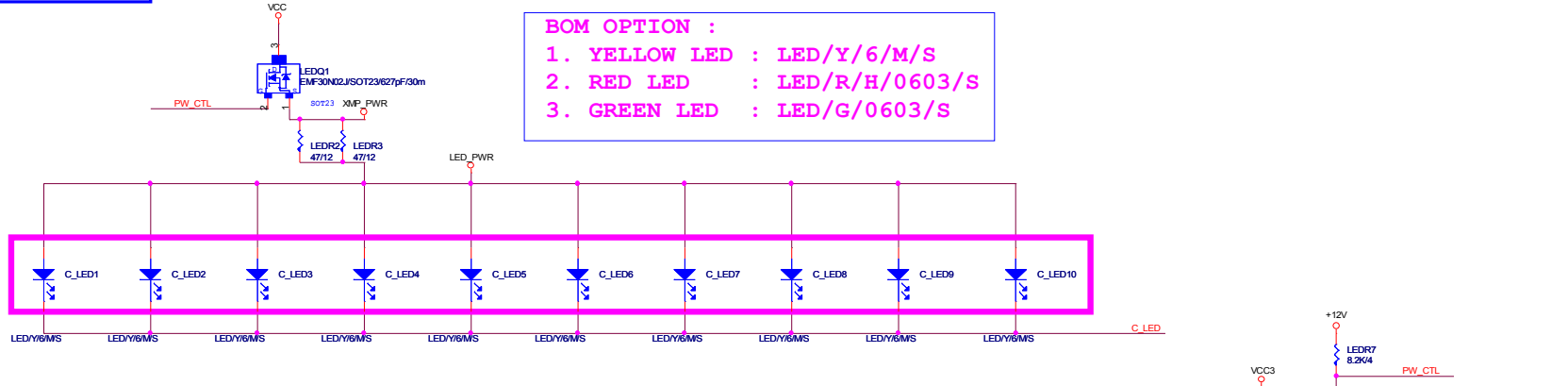


SATA/M.2 LED



SPKR

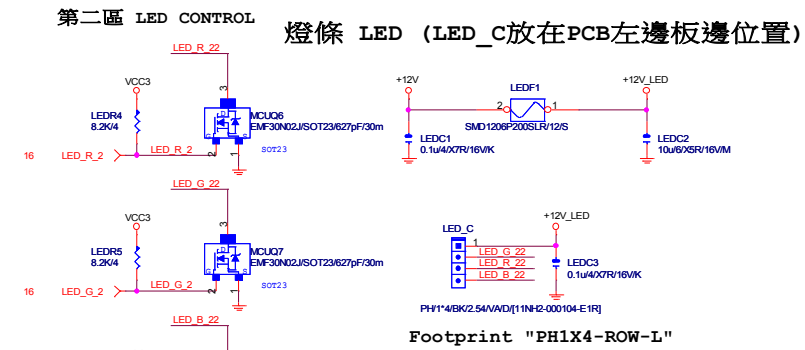




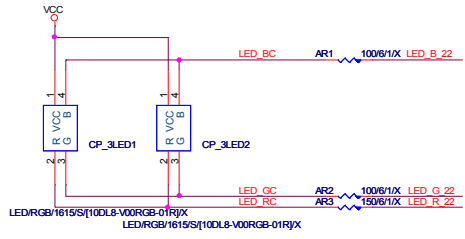
Ambient LED Control

	N_GPP_D22	IO_GP91
Still Mode	H	L
OFF Mode	L	L
Pluse Mode	H	BREATH

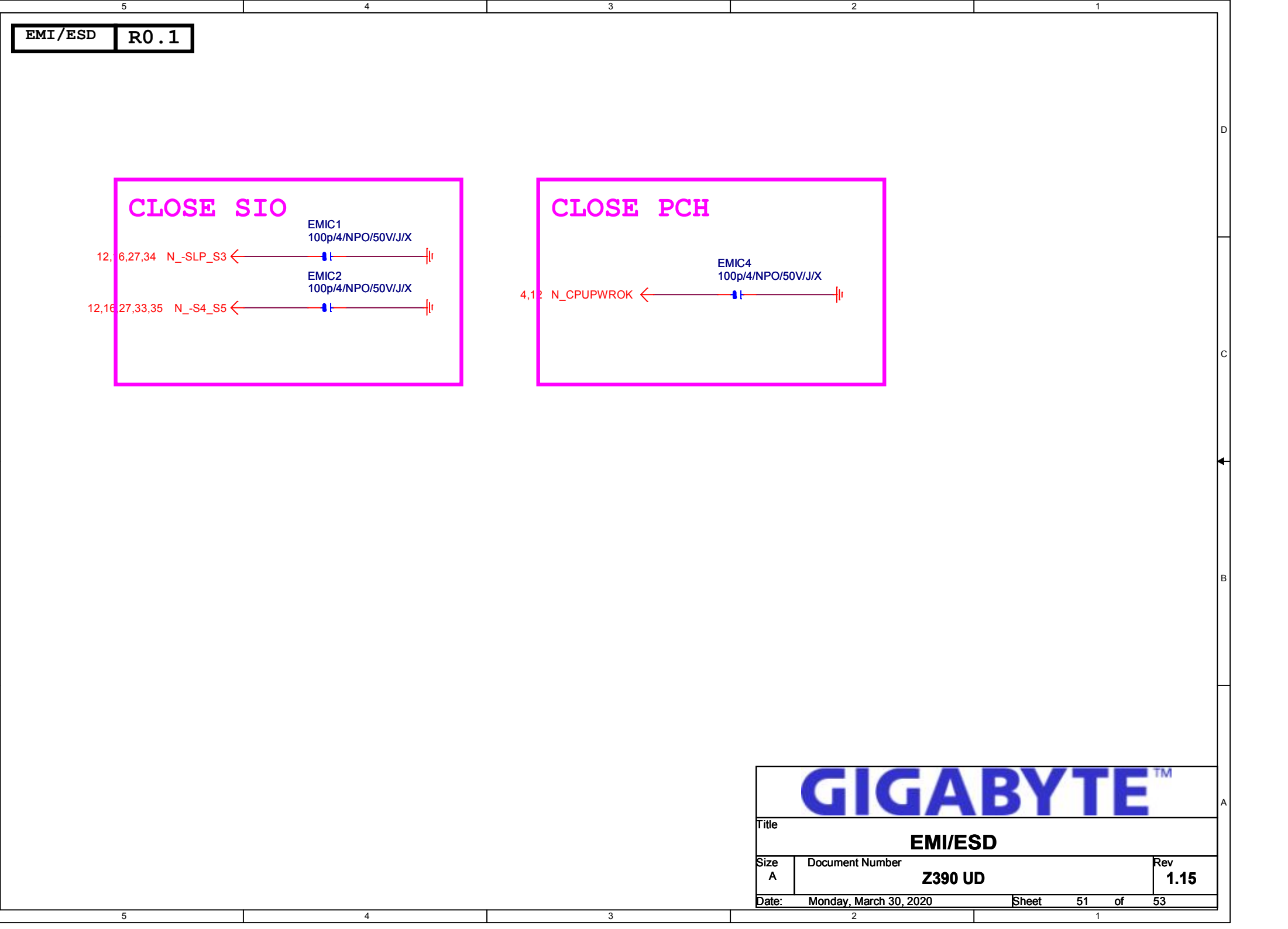
RGB LED CONTROL



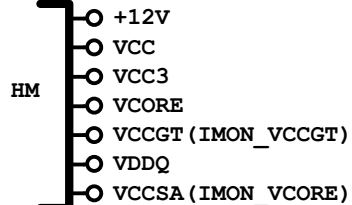
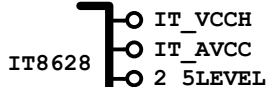
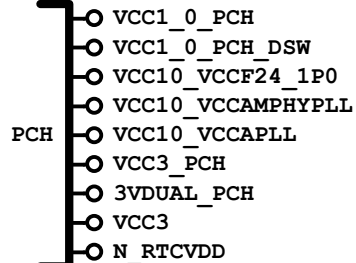
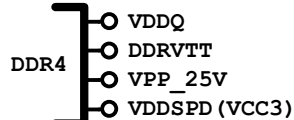
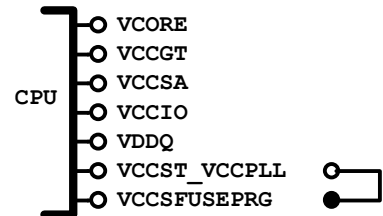
FOR 裝甲高亮度 正發光 LED*2
(位置在正板, 依據裝甲設計擺放)



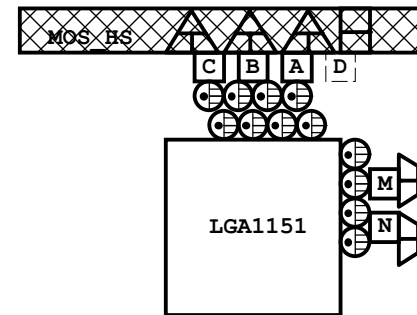
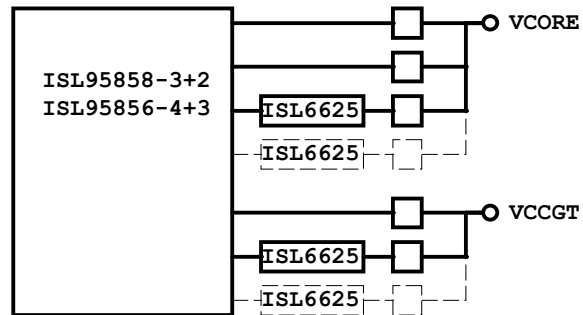
FOOTPRINT: LED_4P_RGB 高亮度



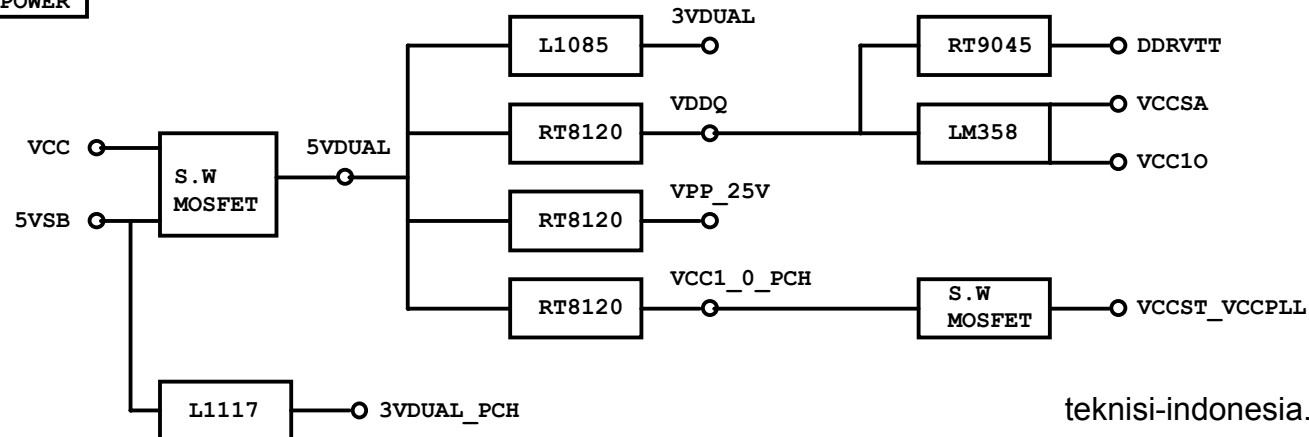
POWER BLOCK MAP



VCORE/VCCGT

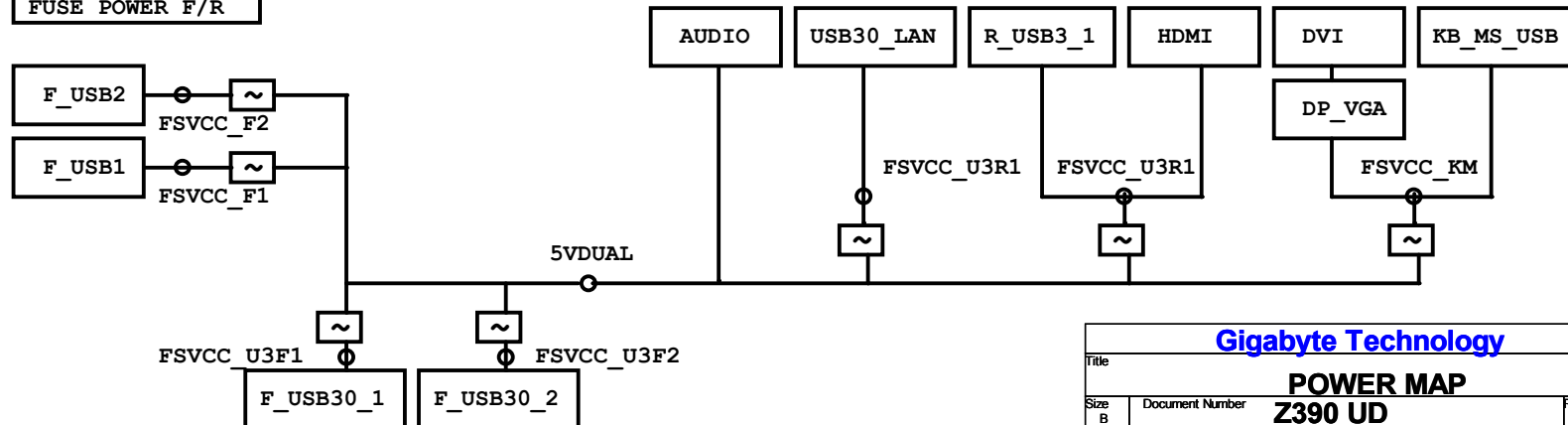


POWER



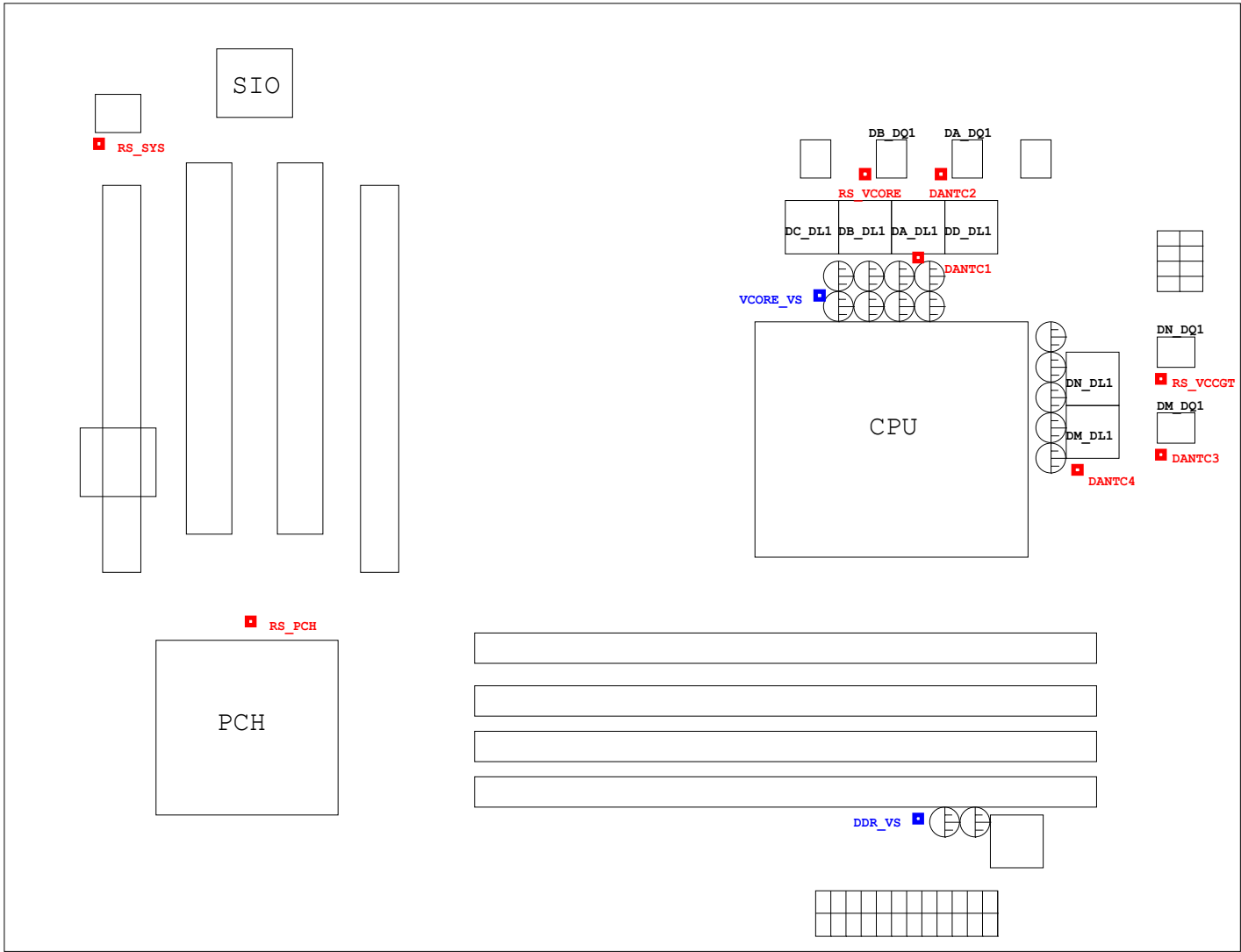
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FUSE POWER F/R



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POWER MAP		
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熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	N/A
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	N/A
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ1	N/A
RS_VCCGT	DN_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A